

N-Ch MOSFET

General Description

WSF80N06 use advanced VD MOST technology to provide low RDS(ON), low gate charge, fast switching This device is specially designed to get better ruggedness and suitable to use in

Low RDS(on) & FOM Extremely low switching loss Excellent stability and uniformity or Invertors

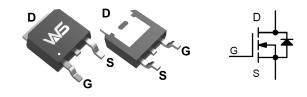
Product Summery

BVDSS	RDSON	ID		
60V	7.2mΩ	80A		

TO-252 Pin Configuration



Consumer electronic power supply Motor control Synchronous-rectification Isolated DC Synchronous-rectification applications



Absolute Maximum Ratings@Tj=25oC(unless otherwise specified)

Symbol	Parameter	Rating	Units	
V _{DS}	Drain-Source Voltage	60	V	
V _{GS}	Gate-Source Voltage	±20	V	
I _D @T _C =25℃	Continuous Drain Current, V _{GS} @ 10V 80			
I _D @T _C =100℃	Continuous Drain Current, V _{GS} @ 10V	43	А	
I _{DM}	Pulsed Drain Current	270	A	
P₀@T₀=25℃	Total Power Dissipation	104	W	
T _{STG}	Storage Temperature Range -55 to 150		°C	
TJ	Operating Junction Temperature Range	-55 to 150	°C	

Thermal Data

Symbol	Parameter	Тур.	Max.	Unit
R _{eja}	Thermal Resistance Junction-Ambient		62.5	°C/W
R _{θJC}	Thermal Resistance Junction-Case		1.2	°C/W



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Electrical Characteristics (TJ=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BVDSS	Drain-Source Breakdown Voltage	VGS=0V , ID=250uA	60			V
RDS(ON)	Static Drain-Source On-Resistance	VGS=10V , ID=45A		7.2	12	mΩ
RDS(ON)		VGS=10V , ID=30A		8.3	15	
VGS(th)	Gate Threshold Voltage	VGS=VDS , ID =250uA	1	1.4	3	V
IDSS	Drain-Source Leakage Current	VDS=60V , VGS=0V , TJ=25 $^\circ\!\mathrm{C}$			10	uA
1055		VDS=48V , VGS=0V , TJ=125℃			250	
IGSS	Gate-Source Leakage Current	VGS=±20V, VDS=0V			±100	nA
Qg	Total Gate Charge	ID=30A		33	45	
Qgs	Gate-Source Charge	VDS=48V		5		nC
Qgd	Gate-Drain Charge	VGS=4.5V		21		
Td(on)	Turn-On Delay Time	VDS=30V		10		
Tr	TRise ime	— ID=30A — RG=3.3Ω		43		20
Td(off)	Turn-Off Delay Time			47		ns
Tf	TFall ime	VGS=10V		80		
Ciss	Input Capacitance	VGS=0V		2680	3300	
Coss	Output Capacitance	VDS=25V		260		pF
Crss	Reverse Transfer Capacitance	f=1.0MHz		180		

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
VSD	Diode Forward Voltage	VGS=0V , IS=45A , TJ=25℃			1.3	V
trr	Reverse Recovery Time			30		nS
Qrr	Reverse Recovery Charge	IF=10A ,dI/dt=100A/µs,TJ=25℃		18		nC



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Typical Characteristics

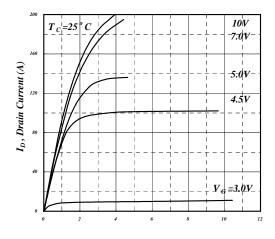


Fig 1. Typical Output Characteristics

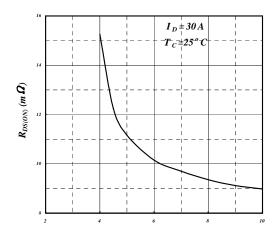
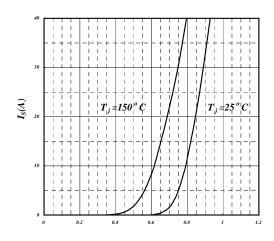
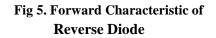


Fig 3. On-Resistance v.s. Gate Voltage





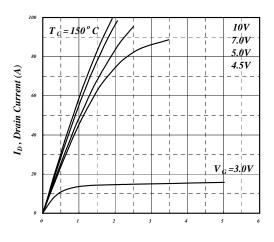


Fig 2. Typical Output Characteristics

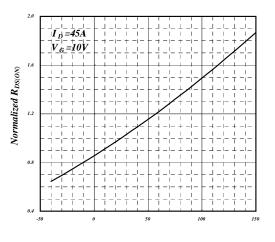
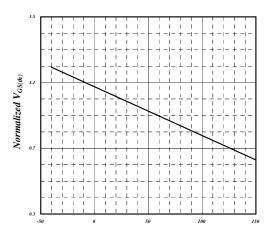
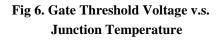


Fig 4. Normalized On-Resistance v.s. Junction Temperature







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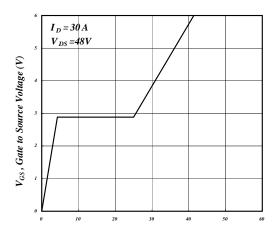


Fig 7. Gate Charge Characteristics

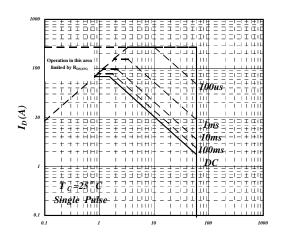
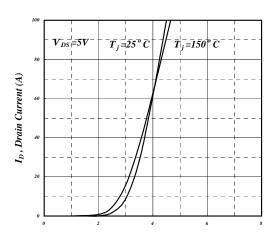


Fig 9. Maximum Safe Operating Area





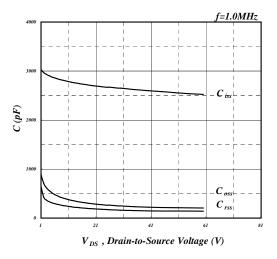


Fig 8. Typical Capacitance Characteristics

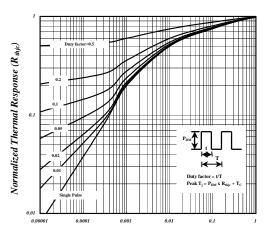


Fig 10. Effective Transient Thermal Impedance

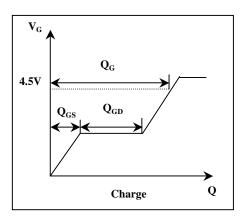


Fig 12. Gate Charge Waveform



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