

General Description

The WST2078 is the highest performance trench N-ch and P-ch MOSFETs with extreme high cell density, which provide excellent RDSON and gate charge for most of the small power switching and load switch applications.

The WST2078 meet the RoHS and Green Product requirement with full function reliability approved.

Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent Cdv/dt effect decline
- Green Device Available

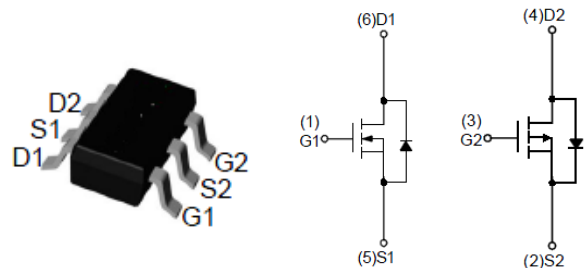
Product Summary

BVDSS	RDSON	ID
20V	30mΩ	5.6A
-20V	65mΩ	-4.5A

Applications

- High Frequency Point-of-Load Synchronous Small power switching for MB/NB/UMPC/VGA
- Networking DC-DC Power System
- Load Switch

SOT-23-6L Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating		Units
		N-Channel	P-Channel	
V_{DS}	Drain-Source Voltage	20	-20	V
V_{GS}	Gate-Source Voltage	± 12	± 12	V
$I_D@T_c=25^\circ C$	Continuous Drain Current, $V_{GS} @ 4.5V^1$	5.6	-4.5	A
$I_D@T_c=70^\circ C$	Continuous Drain Current, $V_{GS} @ 4.5V^1$	4	-2.6	A
I_{DM}	Pulsed Drain Current ²	20	-13	A
$P_D@T_A=25^\circ C$	Total Power Dissipation ³	1.4	1.4	W
T_{STG}	Storage Temperature Range	-55 to 150	-55 to 150	$^\circ C$
T_J	Operating Junction Temperature Range	-55 to 150	-55 to 150	$^\circ C$

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-ambient ¹	---	125	$^\circ C/W$
$R_{\theta JC}$	Thermal Resistance Junction-Case ¹	---	70	$^\circ C/W$

N-Channel Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	20	---	---	V
ΔBV _{DSS} /ΔT _J	BVDSS Temperature Coefficient	Reference to 25°C, I _D =1mA	---	0.024	---	V/°C
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =4.5V, I _D =5A	---	30	38	mΩ
		V _{GS} =2.5V, I _D =4A	---	40	54	
		V _{GS} =1.8V, I _D =1A	---	60	85	
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =250uA	0.5	0.7	1	V
ΔV _{GS(th)}	V _{GS(th)} Temperature Coefficient		---	-2.51	---	mV/°C
I _{DSS}	Drain-Source Leakage Current	V _{DS} =16V, V _{GS} =0V, T _J =25°C	---	---	1	uA
		V _{DS} =16V, V _{GS} =0V, T _J =55°C	---	---	5	
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±8V, V _{DS} =0V	---	---	±100	nA
g _{fs}	Forward Transconductance	V _{DS} =5V, I _D =3A	---	10	---	S
R _g	Gate Resistance	V _{DS} =0V, V _{GS} =0V, f=1MHz	---	2.2	3.4	Ω
Q _g	Total Gate Charge (4.5V)	V _{DS} =10V, V _{GS} =10V, I _D =5A	---	9	---	nC
Q _{gs}	Gate-Source Charge		---	0.3	---	
Q _{gd}	Gate-Drain Charge		---	2	---	
T _{d(on)}	Turn-On Delay Time	V _{DD} =10V, V _{GEN} =4.5V, R _G =6Ω I _D =3A R _L =10Ω	---	2.4	4.3	ns
T _r	Rise Time		---	13	23	
T _{d(off)}	Turn-Off Delay Time		---	15.5	28	
T _f	Fall Time		---	3	5.5	
C _{iss}	Input Capacitance	V _{DS} =10V, V _{GS} =0V, f=1MHz	---	275	---	pF
C _{oss}	Output Capacitance		---	70	---	
C _{rss}	Reverse Transfer Capacitance		---	60	---	

Drain-Source Body Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I _S	Continuous Source-Drain Diode Current ^{1,4}	V _G =V _D =0V, Force Current	---	---	1.0	A
I _{SM}	Pulsed Diode Forward Current ^{2,4}		---	---	20	A
V _{SD}	Body Diode Voltage ²	V _{GS} =0V, I _S =1A, T _J =25°C	---	---	1.1	V
t _{rr}	Reverse Recovery Time	I _F =5A, dI/dt=100A/μs, T _J =25°C	---	10.5	---	nS
Q _{rr}	Reverse Recovery Charge		---	3.2	---	nC

Note :

1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.

2.The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%

3.The power dissipation is limited by 150°C junction temperature

4.The data is theoretically the same as I_D and I_{DM}, in real applications, should be limited by total power dissipation.

P-Channel Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =-250uA	-20	---	---	V
ΔBV _{DSS} /ΔT _J	BVDSS Temperature Coefficient	Reference to 25°C, I _D =-1mA	---	-0.014	---	V/°C
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =-4.5V, I _D =-3A	---	65	85	mΩ
		V _{GS} =-2.5V, I _D =-2A	---	90	120	
		V _{GS} =-1.8V, I _D =-1.5A	---	130	210	
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =-250uA	-0.3	-0.5	-1.0	V
ΔV _{GS(th)}	V _{GS(th)} Temperature Coefficient		---	2.3	---	mV/°C
I _{DSS}	Drain-Source Leakage Current	V _{DS} =-16V, V _{GS} =0V, T _J =25°C	---	---	1	uA
		V _{DS} =-16V, V _{GS} =0V, T _J =55°C	---	---	5	
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±8V, V _{DS} =0V	---	---	±100	nA
g _{fs}	Forward Transconductance	V _{DS} =-5V, I _D =-3A	---	3.7	---	S
Q _g	Total Gate Charge (-4.5V)	V _{DS} =-10V, V _{GS} =-10V, I _D =-3.3A	---	4.5	---	nC
Q _{gs}	Gate-Source Charge		---	0.5	---	
Q _{gd}	Gate-Drain Charge		---	1.5	---	
T _{d(on)}	Turn-On Delay Time	V _{DD} =-10V, V _{GEN} =-10V, R _G =6Ω I _D =-1A, R _L =10Ω.	---	5.3	---	ns
T _r	Rise Time		---	14.2	---	
T _{d(off)}	Turn-Off Delay Time		---	22	---	
T _f	Fall Time		---	4.6	---	
C _{iss}	Input Capacitance	V _{DS} =-10V, V _{GS} =0V, f=1MHz	---	310	---	pF
C _{oss}	Output Capacitance		---	66	---	
C _{rss}	Reverse Transfer Capacitance		---	54	---	

Drain-Source Body Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I _S	Continuous Source-Drain Diode Current ^{1,4}	V _G =V _D =0V, Force Current	---	---	-3.3	A
I _{SM}	Pulsed Diode Forward Current ^{2,4}		---	---	-13	A
V _{SD}	Body Diode Voltage ²	V _{GS} =0V, I _S =-1A, T _J =25°C	---	---	-1.1	V
t _{rr}	Reverse Recovery Time	I _F =-3.3A, dI/dt=100A/μs, T _J =25°C	---	20	---	nS
Q _{rr}	Reverse Recovery Charge		---	6	---	nC

Note :

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%
- 3.The power dissipation is limited by 150°C junction temperature
- 4.The data is theoretically the same as I_D and I_{DM}, in real applications, should be limited by total power dissipation.

N-Channel Typical Characteristics

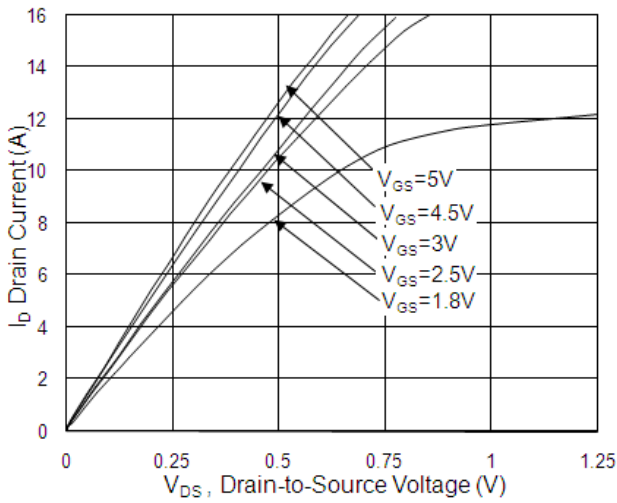


Fig.1 Typical Output Characteristics

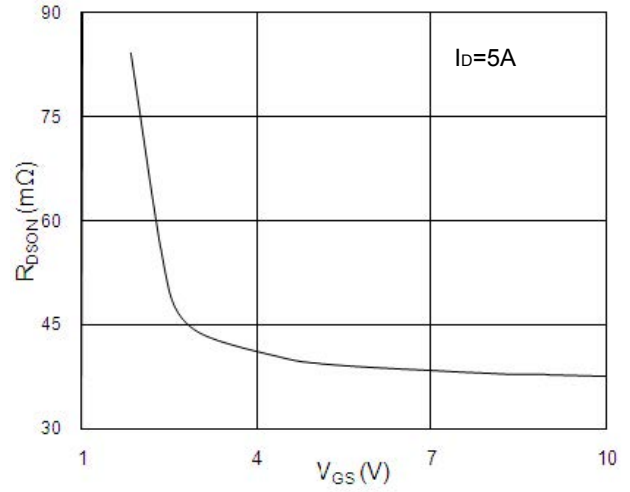


Fig.2 On-Resistance vs. Gate-Source

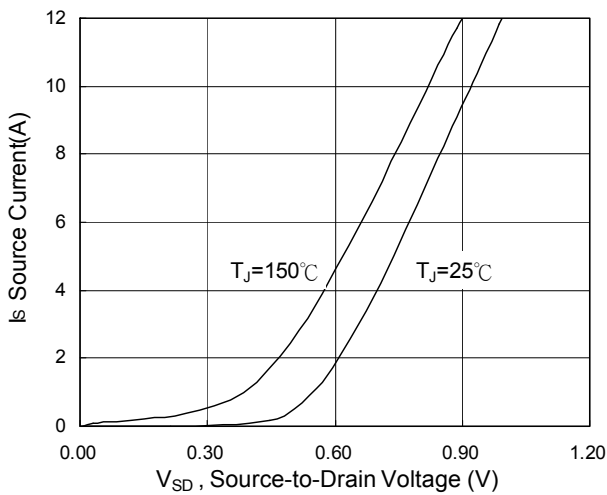


Fig.3 Forward Characteristics Of Reverse

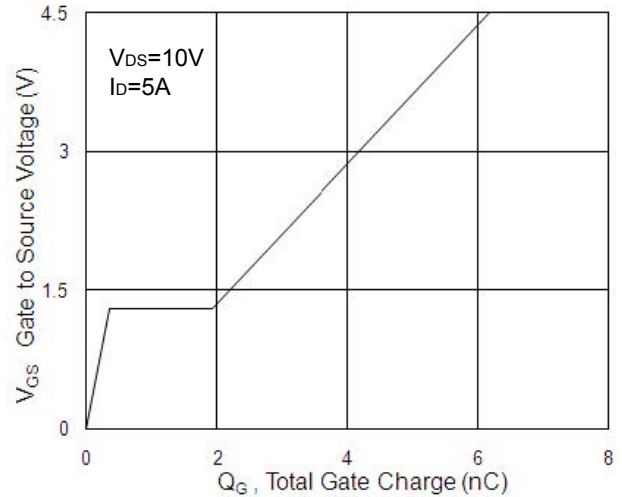


Fig.4 Gate-Charge Characteristics

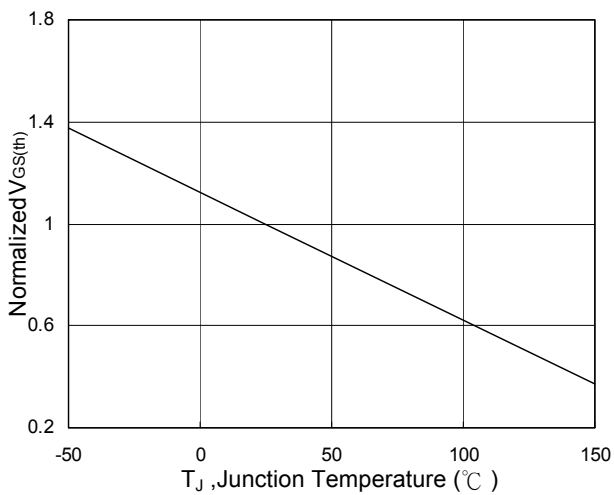


Fig.5 Normalized $V_{GS(th)}$ vs. T_J

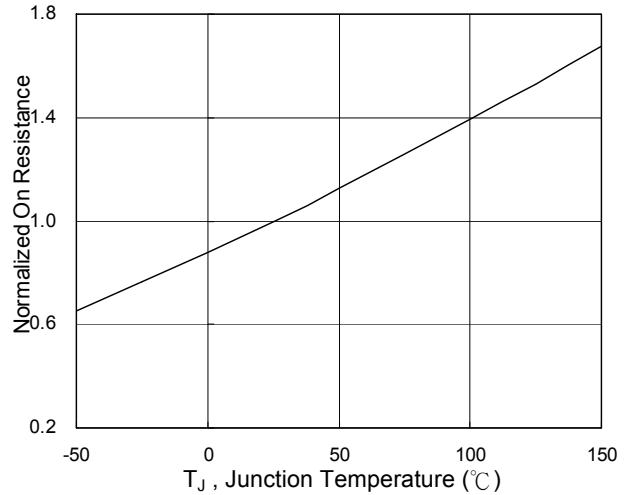


Fig.6 Normalized R_{DSON} vs. T_J

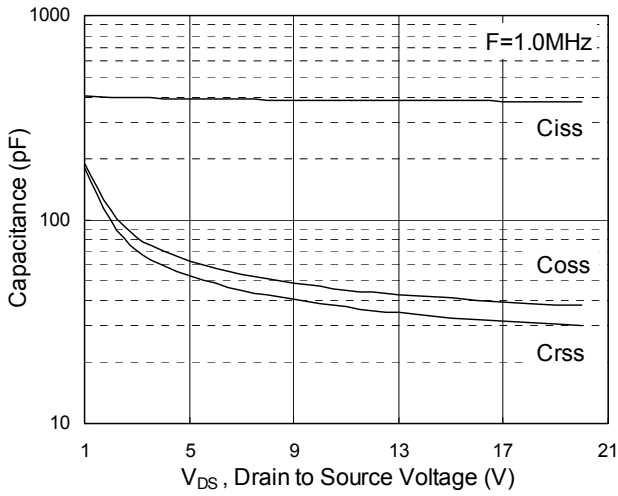


Fig.7 Capacitance

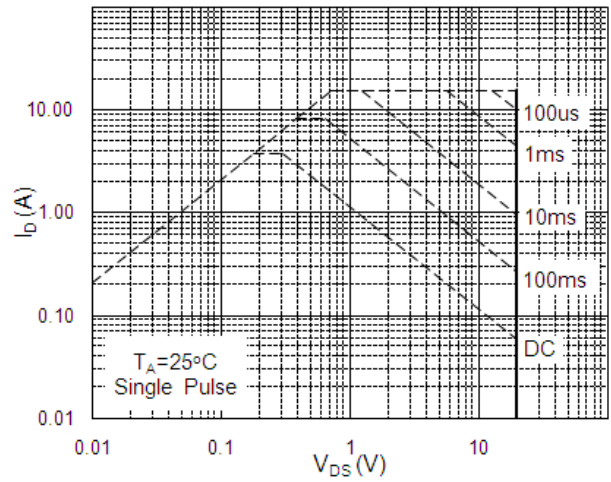


Fig.8 Safe Operating Area

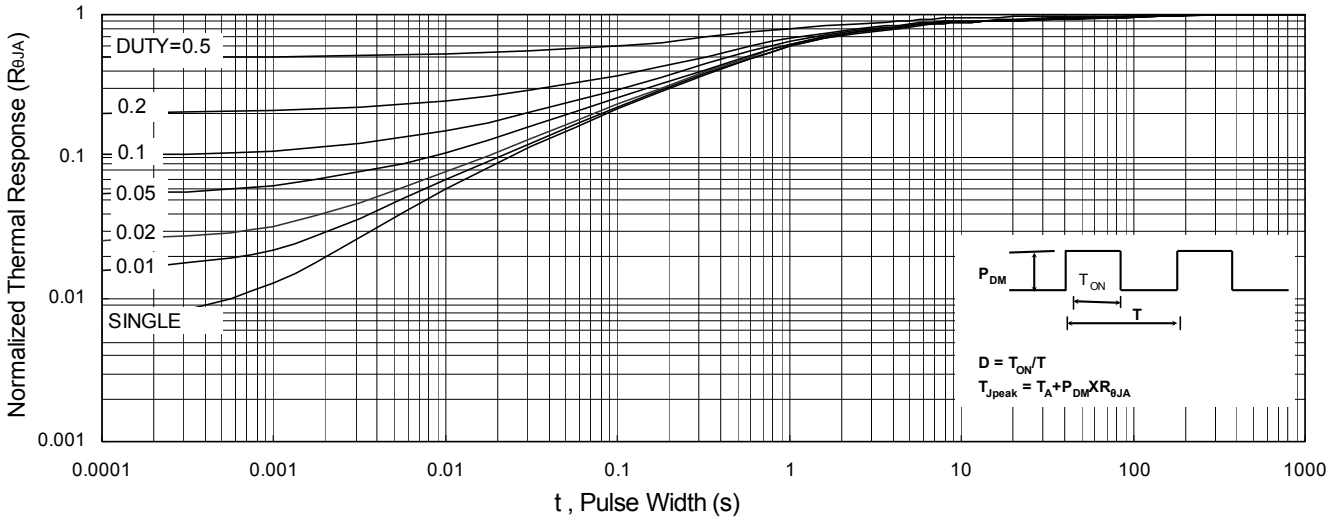


Fig.9 Normalized Maximum Transient Thermal Impedance

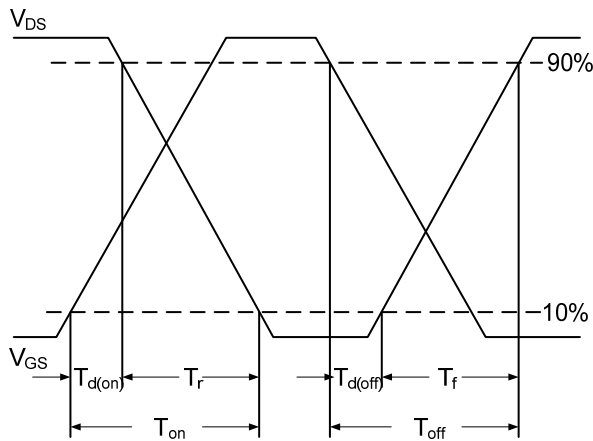


Fig.10 Switching Time Waveform

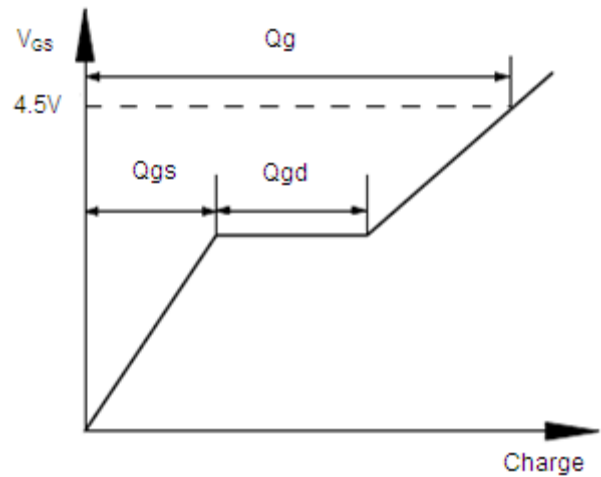


Fig.11 Gate Charge Waveform

P-Channel Typical Characteristics

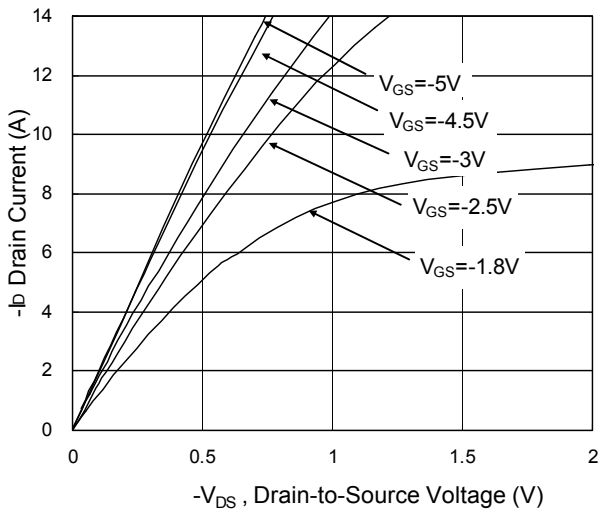


Fig.1 Typical Output Characteristics

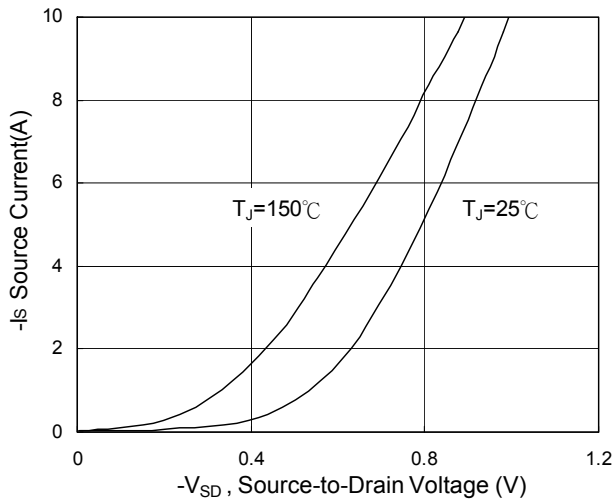
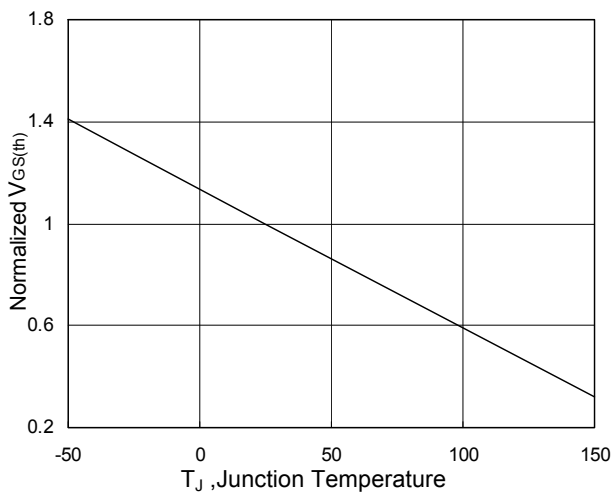


Fig.3 Forward Characteristics Of Reverse



($^\circ C$) Fig.5 Normalized $V_{GS(th)}$ vs. T_J

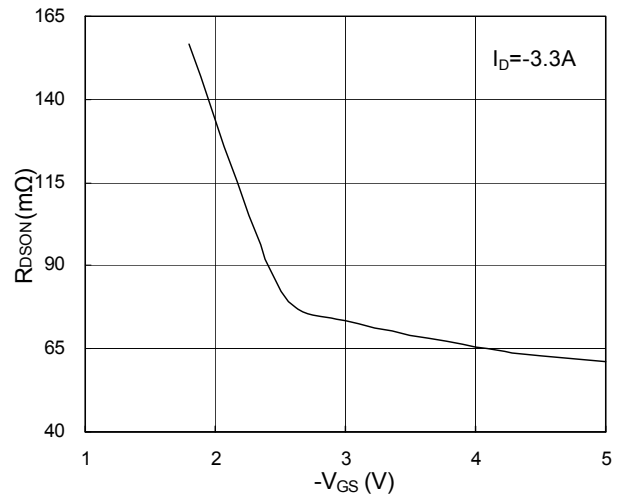


Fig.2 On-Resistance vs. Gate-Source

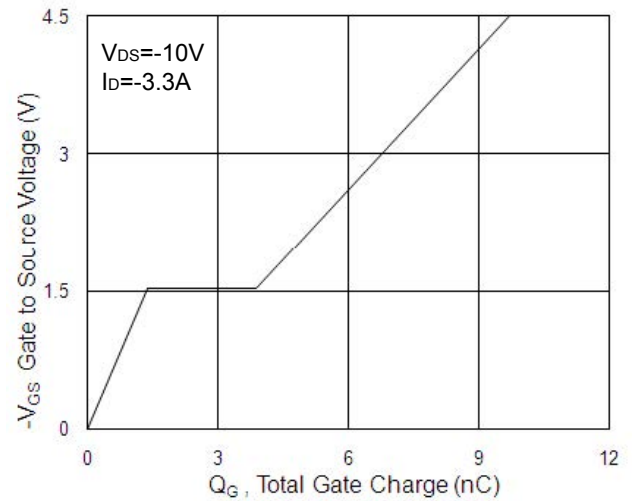


Fig.4 Gate-Charge Characteristics

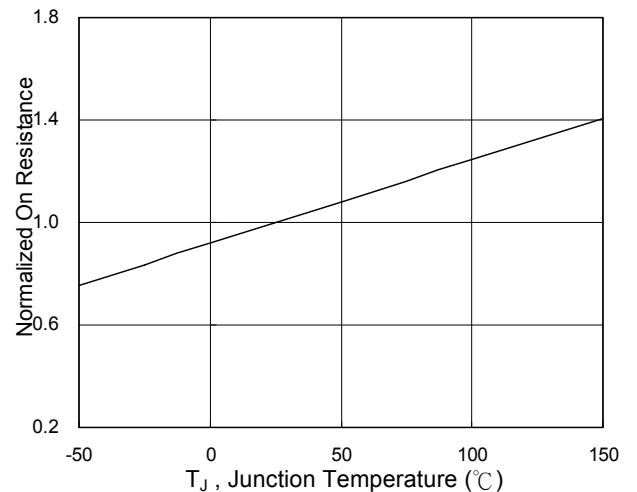


Fig.6 Normalized R_{bSON} vs. T_J

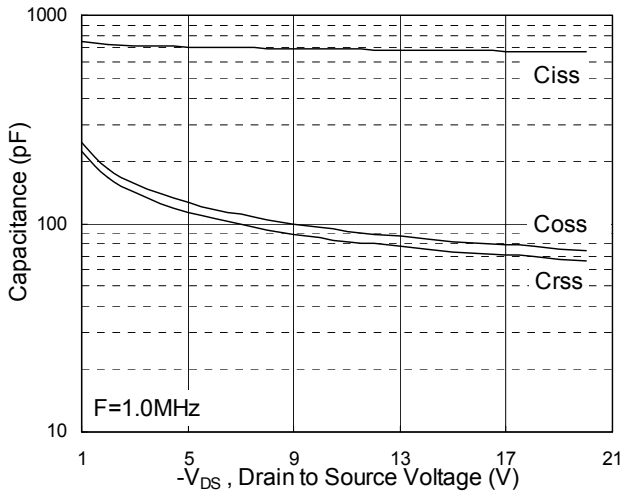


Fig.7 Capacitance

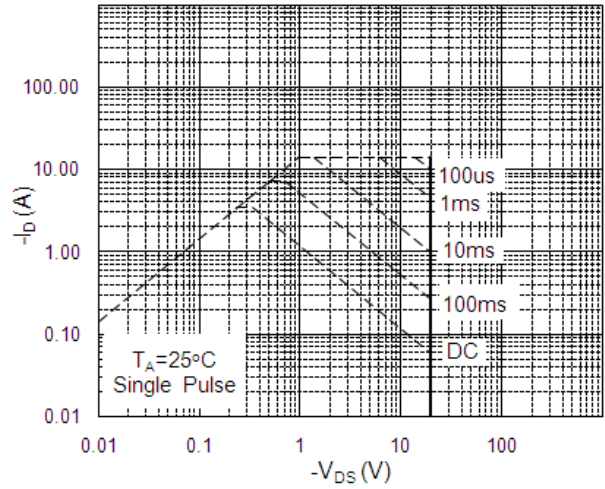


Fig.8 Safe Operating Area

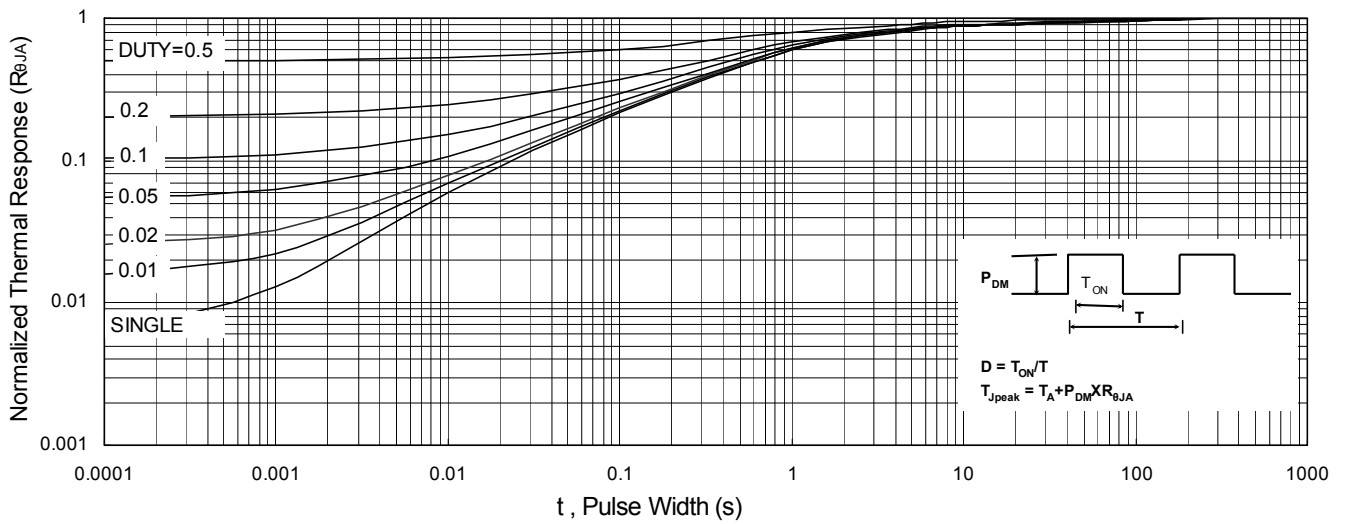


Fig.9 Normalized Maximum Transient Thermal Impedance

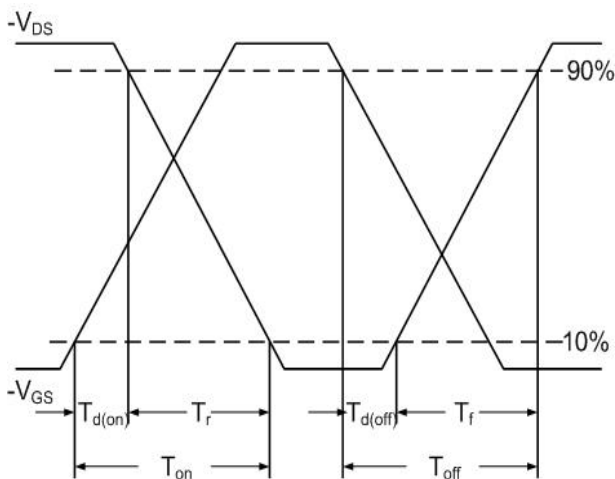


Fig.10 Switching Time Waveform

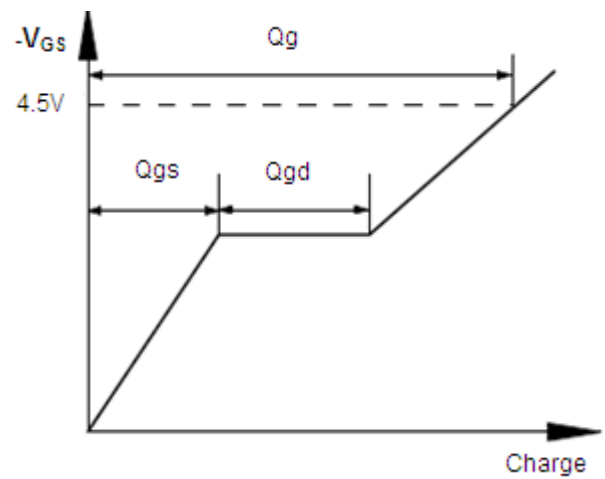


Fig.11 Gate Charge Waveform



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