

# **General Description**

The WWSD3072DN the highest performance trench N-ch MOSFETs with extreme high cell density, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications.

The WSD3072DN meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

#### **Features**

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- 100% EAS Guaranteed
- Green Device Available

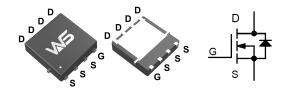
#### **Product Summery**

BVDSS	RDSON	ID
30V	$3.5 \text{m}\Omega$	72A

# **Applications**

- High Frequency Point-of-Load Synchronous Buck Converter for MB/NB/UMPC/VGA
- Networking DC-DC Power System
- Load Switch

# **DFN3X3-8L Pin Configuration**



# **Absolute Maximum Ratings**

Symbol	Parameter Rating		Units
$V_{DS}$	Drain-Source Voltage	30	V
$V_{GS}$	Gate-Source Voltage	±20	V
I <sub>D</sub> @T <sub>C</sub> =25℃	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	72	Α
I <sub>D</sub> @T <sub>C</sub> =100℃	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	48	Α
I <sub>D</sub> @T <sub>A</sub> =25℃	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	19	Α
I <sub>D</sub> @T <sub>A</sub> =70℃	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	15	Α
I <sub>DM</sub> @Тс=25°С	Pulsed Drain Current <sup>2</sup>	160	Α
EAS	Avalanche Energy ,Single Pulse (L=0.5mH) <sup>3</sup>	315	mJ
I <sub>AS</sub>	Avalanche Current ,Single pulse(L=0.5mH) <sup>3</sup>	38	Α
P <sub>D</sub> @T <sub>C</sub> =25℃	Total Power Dissipation <sup>4</sup>	58	W
P <sub>D</sub> @T <sub>A</sub> =25℃	Total Power Dissipation <sup>4</sup>	6.08	W
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	$^{\circ}$
TJ	Operating Junction Temperature Range -55 to 150		${\mathbb C}$

## **Thermal Data**

Symbol	Parameter	Тур.	Max.	Unit
$R_{ heta JA}$	Thermal Resistance Junction-Ambient <sup>1</sup>		62	°C/W
R <sub>eJC</sub>	Thermal Resistance Junction-Case <sup>1</sup>		2.1	°C/W



# Electrical Characteristics (T<sub>J</sub>=25 ℃, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =250uA	30			V
$\triangle BV_{DSS}/\triangle T_{J}$	BVDSS Temperature Coefficient	Reference to 25℃, I <sub>D</sub> =1mA		0.028		V/°C
В	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =10V , I <sub>D</sub> =30A		3.5	4.5	mΩ
$R_{DS(ON)}$		V <sub>GS</sub> =4.5V , I <sub>D</sub> =15A		6.5	8.5	
V <sub>GS(th)</sub>	Gate Threshold Voltage	\/ -\/   -250\\A	1.0	1.6	2.5	V
$\triangle V_{GS(th)}$	V <sub>GS(th)</sub> Temperature Coefficient	$V_{GS}=V_{DS}$ , $I_D=250uA$		-6.16		mV/℃
	Drain-Source Leakage Current	V <sub>DS</sub> =24V , V <sub>GS</sub> =0V , T <sub>J</sub> =25℃			1	
I <sub>DSS</sub>		V <sub>DS</sub> =24V , V <sub>GS</sub> =0V , T <sub>J</sub> =55℃			5	uA uA
I <sub>GSS</sub>	Gate-Source Leakage Current	$V_{GS}$ = $\pm 20 V$ , $V_{DS}$ = $0 V$			±100	nA
gfs	Forward Transconductance	V <sub>DS</sub> =5V , I <sub>D</sub> =40A		36		S
$R_g$	Gate Resistance	V <sub>DS</sub> =0V , V <sub>GS</sub> =0V , f=1MHz		1.7		Ω
Qg	Total Gate Charge (4.5V)			20		
$Q_gs$	Gate-Source Charge	VDS=15V, VGS=4.5V, ID=15A		7.6		nC
Q <sub>gd</sub>	Gate-Drain Charge			7.2		
T <sub>d(on)</sub>	Turn-On Delay Time			7.8		
T <sub>r</sub>	Rise Time	V <sub>DD</sub> =15V, RL=15Ω ,		15		
T <sub>d(off)</sub>	Turn-Off Delay Time	I <sub>D</sub> =15A, V <sub>GS</sub> =10V,		10.6		ns
T <sub>f</sub>	Fall Time	Rg=3.3Ω		37.3		
C <sub>iss</sub>	Input Capacitance			2295		
C <sub>oss</sub>	Output Capacitance	V <sub>DS</sub> =15V , V <sub>GS</sub> =0V , f=1MHz		267		pF
C <sub>rss</sub>	Reverse Transfer Capacitance			210		

# **Diode Characteristics**

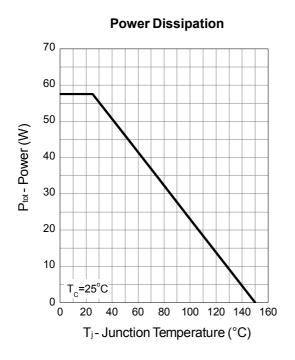
Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current <sup>1,6</sup>	V <sub>G</sub> =V <sub>D</sub> =0V , Force Current			80	Α
I <sub>SM</sub>	Pulsed Source Current <sup>2,6</sup>				160	Α
V <sub>SD</sub>	Diode Forward Voltage <sup>2</sup>	V <sub>GS</sub> =0V , I <sub>S</sub> =1A , T <sub>J</sub> =25℃			1.1	V
t <sub>rr</sub>	Reverse Recovery Time	IF=30A , dI/dt=100A/µs		14		nS
Q <sub>rr</sub>	Reverse Recovery Charge			5		nC

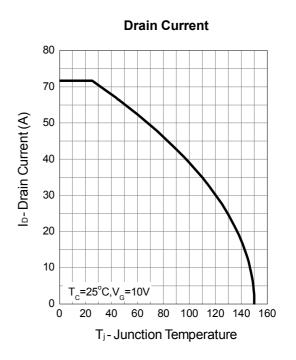
Note d : Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2\%.$ 

Note e: Guaranteed by design, not subject to production testing.

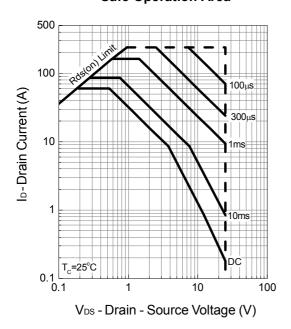


# **Typical Operating Characteristics**

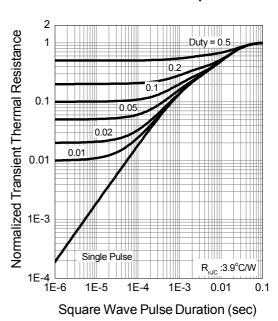




## **Safe Operation Area**

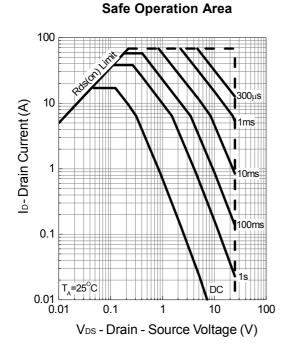


#### **Thermal Transient Impedance**

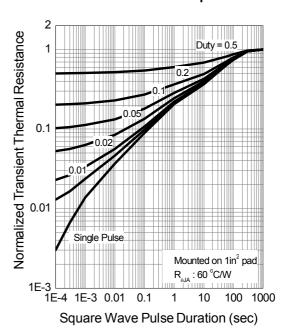




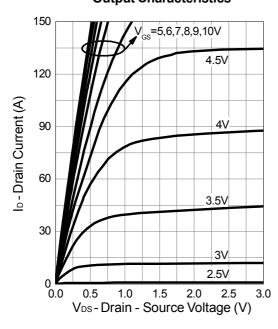
# **Typical Operating Characteristics**



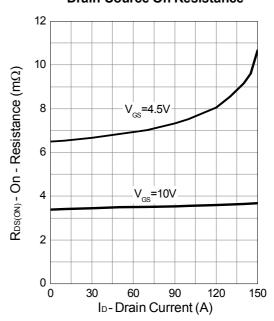
**Thermal Transient Impedance** 



**Output Characteristics** 

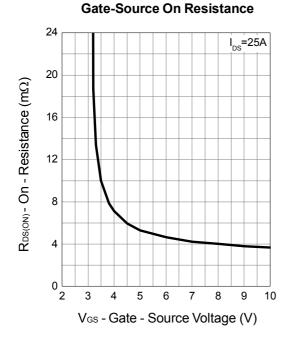


**Drain-Source On Resistance** 

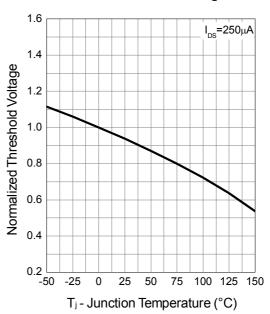




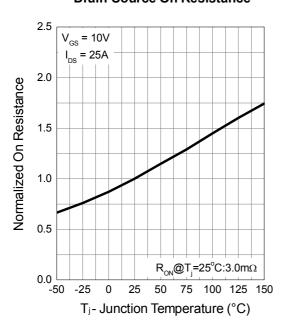
# **Typical Operating Characteristics (Cont.)**



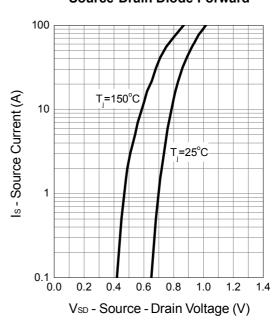
# **Gate Threshold Voltage**



### **Drain-Source On Resistance**

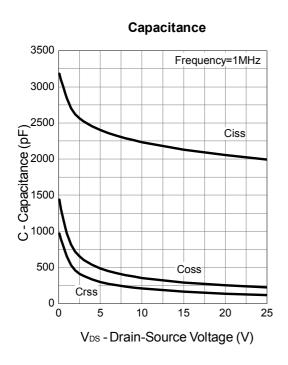


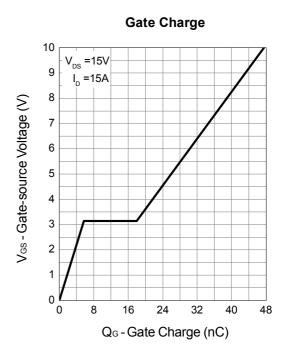
### Source-Drain Diode Forward



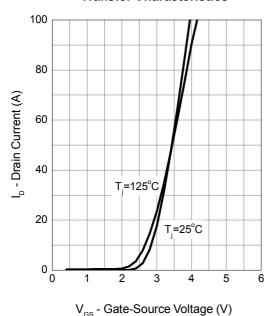


# **Typical Operating Characteristics (Cont.)**





## **Transfer Characteristics**





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