

Dual N-Channel MOSFET

General Description

Features

The WSP4984 is the highest performance trench N-Channel MOSFET with extreme high cell density, which provide excellent $R_{DS(ON)}$ and gate charge for most of the synchronous buck converter applications.

The WSP4984 meet the RoHS and Green Product requirement, 100% E_{AS} guaranteed with full function reliability approved.

Advanced high cell density Trench technology

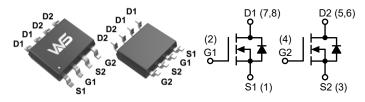
Product Summery

BV _{DSS}	R _{DS(ON)}	Ι _D
40V	15mΩ	10A

Applications

- White LED boost converters
- Automotive Systems
- Industrial DC/DC Conversion Circuits

SOP-8 Pin Configuration



Symbol Parameter Units Rating V_{DS} **Drain-Source Voltage** 40 V V_{GS} Gate-Source Voltage ±20 $I_D@T_C=25^{\circ}C$ Continuous Drain Current, V_{GS} @ 10V¹ 10 $I_D@T_C=70^{\circ}C$ Continuous Drain Current, V_{GS} @ 10V¹ 8 А Pulsed Drain Current² 50 I_{DM} P_D@T_A=25°C **Total Power Dissipation** 2.0 W P_D@T_A=70°C 1.3 **Total Power Dissipation** Storage Temperature Range -55 to 150 T_{STG} °C T_J **Operating Junction Temperature Range** -55 to 150

Absolute Maximum Ratings

Super Low Gate Charge

• 100% E_{AS} Guaranteed

• Green Device Available

• Excellent CdV/dt effect decline

Thermal Data

Symbol	Parameter	Тур.	Max.	Units	
R _{θJA}	Thermal Resistance, Junction-to-Ambient ¹		90	°C/W	
R _{θJC}	Thermal Resistance, Junction-to-Case ¹		40	C/VV	



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Electrical Characteristics (T_J=25°C, Unless Otherwise Noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units	
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =250µA	40			V	
P	Static Drain-Source On-Resistance ²	V _{GS} =10V , I _D =6.6A					
R _{DS(ON)}		V _{GS} =4.5V , I _D =5.9A		17.7	21	- mΩ	
V _{GS(th)}	Gate Threshold Voltage	$V_{GS}=V_{DS}$, $I_{D}=250\mu A$	1.55	2.2	2.7	V	
	Drain-Source Leakage Current	V_{DS} =24V , V_{GS} =0V , T_{J} =25°C			1.0	μΑ	
I _{DSS}		V_{DS} =24V , V_{GS} =0V , T_{J} =55°C			5.0		
I _{GSS}	Gate-Source Leakage Current	V_{GS} =±20V , V_{DS} =0V			±100	nA	
9 _{fs}	Forward Transconductance	V _{DS} =15V,I _D =6.6A		50		S	
Qg	Total Gate Charge (4.5V)	V _{DS} =15V,V _{GS} =4.5V,I _D =8.8A	10	13.6	16	nC	
Q _{gs}	Gate-Source Charge		3.6	4.5	5.4		
Q _{gd}	Gate-Drain Charge		3.8	6.4	9		
T _{d(on)}	Turn-On Delay Time			6.4			
Tr	Rise Time	V _{DD} =15V,V _{GEN} =10V,		17		- ns	
T _{d(off)}	Turn-Off Delay Time	R_{G} =6 Ω , I_{D} =1A , R_{L} =15 Ω		29.6			
Τ _f	Fall Time			16.8			
C _{iss}	Input Capacitance		1200	1500	1950		
C _{oss}	Output Capacitance	V _{DS} =15V , V _{GS} =0V , f = 1.0MHz	150	250		pF	
C _{rss}	Reverse Transfer Capacitance			135			

Note:

1. Surface Mounted on 1" x 1" FR4 Board.

2. Pulse test: PW \leq 300us duty cycle \leq 2%.

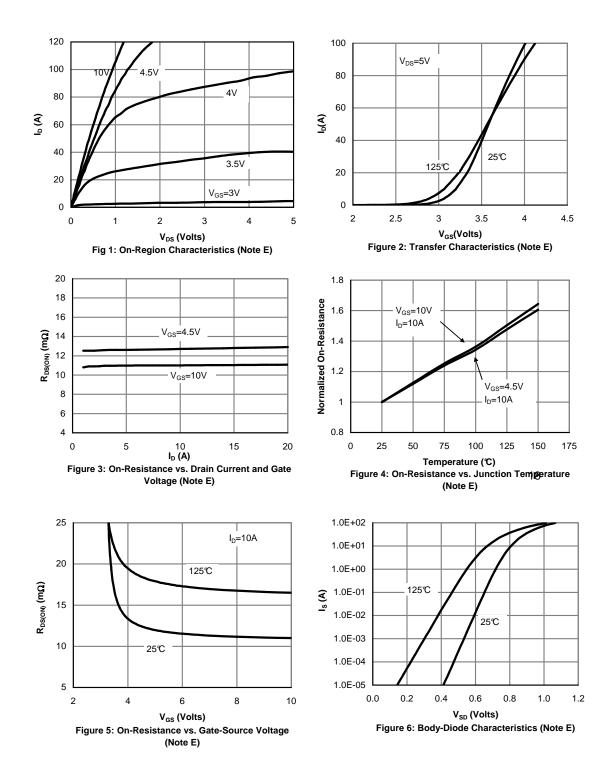
3. Pulse width limited by maximum junction temperature.

4. Guaranteed by design, not subject to production testing.



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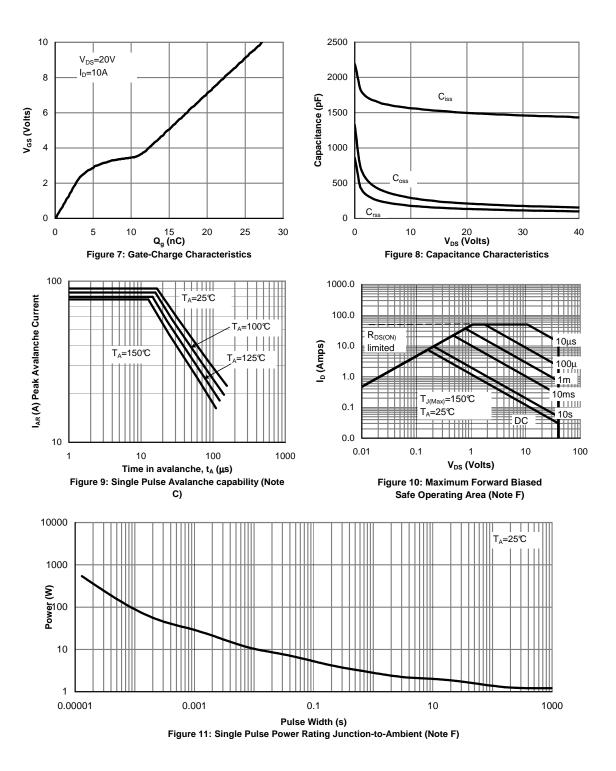
Typical Characteristics





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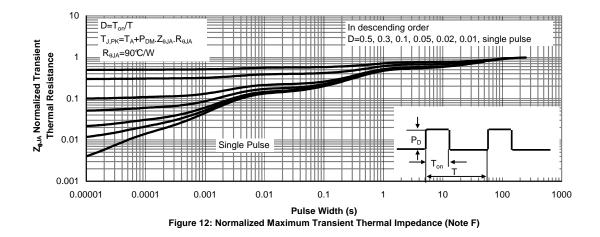
Typical Characteristics (Cont.)





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Typical Characteristics (Cont.)

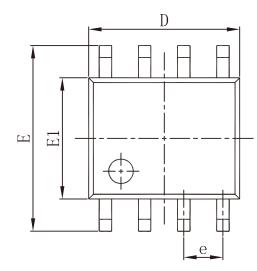


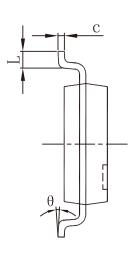


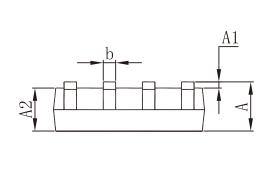


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Packaging information







SYMBOL	MILLIMETERS		INCHES		
	MIN.	MAX.	MIN.	MAX.	
A	1.350	1.750	0.053	0.069	
A1	0.100	0.250	0.004	0.010	
A2	1.350	1.550	0.053	0.061	
b	0.330	0.510	0.013	0.020	
с	0.170	0.250	0.007	0.010	
D	4.800	5.000	0.189	0.197	
е	1.27 BSC		0.050 BSC		
E	5.800	6.200	0.228	0.244	
E1	3.800	4.000	0.150	0.157	
L	0.400	1.270	0.016	0.050	
θ	0°	8°	0°	8°	



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