

Dual N-Channel MOSFET

General Description

The WST3392 is the highest performance trench Dual N-Channel MOSFETs with extreme high cell density, which provide excellent $R_{DS(ON)}$ and gate charge for most of the small power switching and load switch applications.

The WST3392 meet the RoHS and Green Product requirement with full function reliability approved.

Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- Green Device Available

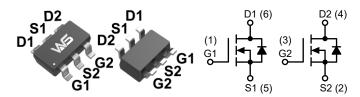
Product Summery

BV _{DSS}	R _{DS(ON)}	I _D
30V	46mΩ	3.7A

Applications

- Power management in portable and battery operated products
- One cell battery pack protection

SOT-23-6L Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	30	V
V _{GS}	Gate-Source Voltage	±20	V
I _D @T _C =25°C	Continuous Drain Current, V _{GS} @ 4.5V ¹	3.7	
I _D @T _C =70°C	Continuous Drain Current, V _{GS} @ 4.5V ¹	3.0	A
I _{DM}	Pulsed Drain Current ²	20	
P _D @T _A =25°C	Power Dissipation ³	1.15	W
T _{STG}	Storage Temperature Range -55 to 150		°C
T_J	Operating Junction Temperature Range	-55 to 150	

Thermal Data

Symbol	Parameter	Тур.	Max.	Units
$R_{ heta JA}$	Thermal Resistance, Junction-to-Ambient ¹		110	°C/W
$R_{ heta JC}$	Thermal Resistance, Junction-to-Case ¹		80	C/VV



Electrical Characteristics (T_J=25°C, Unless Otherwise Noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units	
BV _{DSS}	Drain-Source Breakdown Voltage	I _D =250μA , V _{GS} =0V	30			V	
	Zaus Cata Valtana Dunin Cumunt	V _{DS} =30V , V _{GS} =0V			1.0		
I _{DSS}	Zero Gate Voltage Drain Current	T _J =55°C			5.0	μA	
I _{GSS}	Gate-Body leakage current	V _{DS} =0V , V _{GS} =±12V			±100	nA	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$, $I_{D}=250\mu A$	0.5	1.0	1.5	V	
I _{D(ON)}	On state drain current	V _{GS} =10V , V _{DS} =5V	20			Α	
		V _{GS} =10V , I _D =3.4A		46	60		
D	Static Ducin Course On Desintance	T _J =125°C		73	88	mΩ	
R _{DS(ON)}	Static Drain-Source On-Resistance	V _{GS} =4.5V , I _D =3A		53	70	1 M22	
		V _{GS} =2.5V , I _D =2A		65	90		
9 _{fs}	Forward Transconductance	V _{DS} =5V , I _D =3.4A		14		S	
R_g	Gate Resistance	V _{DS} =0V , V _{GS} =0V , f = 1.0MHz		4.3		Ω	
Q _g (10V)	Total Gate Charge			10			
Q _g (4.5V)	Total Gate Charge	\\ -10\\ \\ -15\\ \ \ -2.4A		4.7		nC	
Q_{gs}	Gate-Source Charge	V_{GS} =10V , V_{DS} =15V , I_{D} =3.4A		0.95			
Q _{gd}	Gate-Drain Charge			1.6			
T _{d(on)}	Turn-On Delay Time			3.5			
T _r	Turn-On Rise Time	V_{GS} =10V , V_{DS} =15V , R_L =4.4 Ω		1.5		- ns	
T _{d(off)}	Turn-Off Delay Time	R_{GEN} =3 Ω		17.5			
T _f	Turn-Off Fall Time			2.5			
C _{iss}	Input Capacitance			235			
C _{oss}	Output Capacitance	V _{GS} =0V , V _{DS} =15V , f = 1.0MHz		35		pF	
C _{rss}	Reverse Transfer Capacitance			18			

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units
I _S	Maximum Body-Diode Continuous Current				1.5	Α
V _{SD}	Diode Forward Voltage	I _S =1A, V _{GS} =0V		0.75	1.0	V
t _{rr}	Body Diode Reverse Recovery Time	l ₌ =3.4A, dl/dt=100A/µs		8.5		ns
Q _{rr}	Body Diode Reverse Recovery Charge	1 _F -3.4A, αι/αι-100A/μS		2.55		nC

Note:

- 1. The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2. The data tested by pulsed , pulse width \leq 300us , duty cycle \leq 2%
- 3. The power dissipation is limited by 150°C junction temperature.
- 4. The data is theoretically the same as I_D and I_{DM} , in real applications, should be limited by total power dissipation.



Typical Characteristics

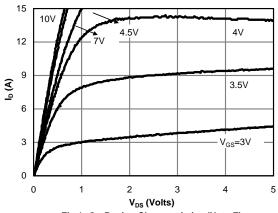


Fig 1: On-Region Characteristics (Note E)

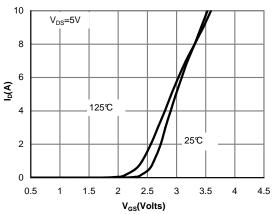


Figure 2: Transfer Characteristics (Note E)

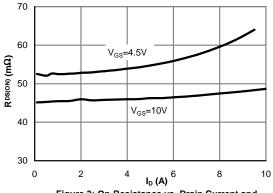


Figure 3: On-Resistance vs. Drain Current and Gate Voltage (Note E)

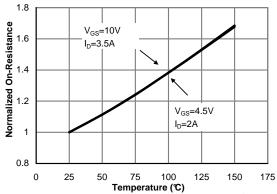


Figure 4: On-Resistance vs. Junction Temp8rature
(Note E)

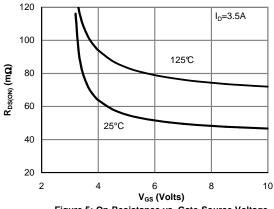


Figure 5: On-Resistance vs. Gate-Source Voltage (Note E)

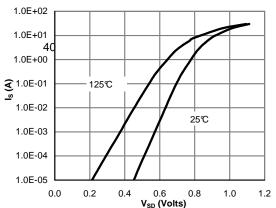


Figure 6: Body-Diode Characteristics (Note E)



Typical Characteristics (Cont.)

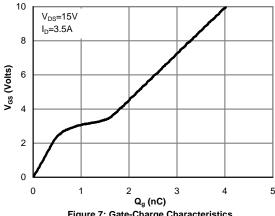


Figure 7: Gate-Charge Characteristics

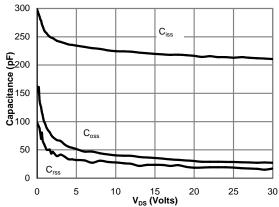


Figure 8: Capacitance Characteristics

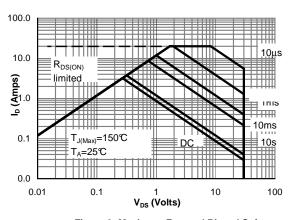
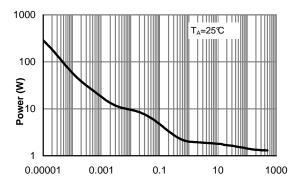


Figure 9: Maximum Forward Biased Safe Operating Area (Note F)



Pulse Width (s) Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note F)

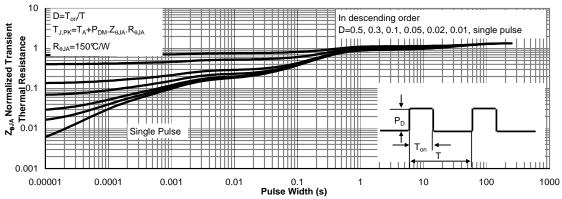
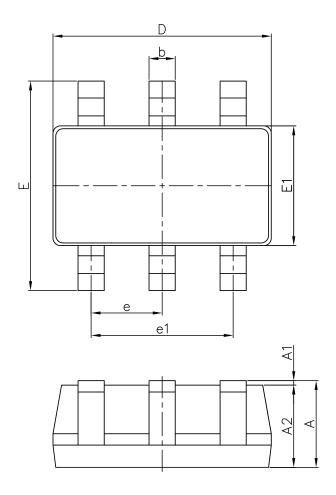
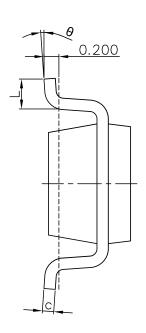


Figure 11: Normalized Maximum Transient Thermal Impedance (Note F)



Packaging information





Sumb al	Dimensions In Millimeters		Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
Α	1.050	1.250	0.041	0.049	
A1	0.000	0.100	0.000	0.004	
A2	1.050	1.150	0.041	0.045	
b	0.300	0.500	0.012	0.020	
С	0.100	0.200	0.004	0.008	
D	2.820	3.020	0.111	0.119	
E1	1.500	1.700	0.059	0.067	
E	2.650	2.950	0.104	0.116	
е	0.950 BSC		0.037 BSC		
e1	1.800	2.000	0.071	0.079	
L	0.300	0.600	0.012	0.024	
θ	0°	8°	0°	8°	



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