

General Description

The WSD13N10TDN33 uses advanced technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 4.5V.

This device is suitable for use as a Battery protection or in other Switching application.

Features

- 100% UIS + R_g Tested.
- Reliable and Rugged
- Lead Free and Green Devices Available (RoHS Compliant)
- Moisture Sensitivity Level MSL1 (per JEDEC J-STD-020D)

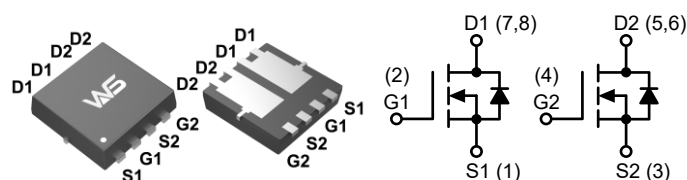
Product Summary

| BV_{DSS} | $R_{DS(ON)}$ | I_D |
|------------|--------------|-------|
| 100V | 70m Ω | 15A |

Applications

- Power Management in Notebook Computer, Portable Equipment and Battery Powered Systems.

DFN3X3-8L Pin Configuration



Absolute Maximum Ratings ($T_A=25^\circ\text{C}$, Unless Otherwise Noted)

| Symbol | Parameter | | Rating | Units |
|-------------------|--|-------------------------|------------|--------------------|
| V_{DS} | Drain-Source Voltage | | 100 | V |
| V_{GS} | Gate-Source Voltage | | ± 20 | |
| I_S | Diode Continuous Forward Current | $T_C=25^\circ\text{C}$ | 15 | A |
| I_D | Continuous Drain Current | $T_C=25^\circ\text{C}$ | 15 | |
| | | $T_C=100^\circ\text{C}$ | 9.4 | |
| I_{DM}^2 | Pulse Drain Current | $T_C=25^\circ\text{C}$ | 45 | W |
| P_D | Maximum Power Dissipation | $T_C=25^\circ\text{C}$ | 23 | |
| | | $T_C=100^\circ\text{C}$ | 9 | |
| $R_{\theta JA}^4$ | Thermal Resistance-Junction to Ambient | Steady State | 95 | $^\circ\text{C/W}$ |
| $R_{\theta JC}$ | Thermal Resistance-Junction to Case | | 5.5 | |
| I_{AS}^3 | Avalanche Current, Single pulse | $L=0.5\text{mH}$ | 8 | A |
| E_{AS}^3 | Avalanche Energy, Single pulse | $L=0.5\text{mH}$ | 16 | mJ |
| T_{STG} | Storage Temperature Range | | -55 to 150 | $^\circ\text{C}$ |
| T_J | Maximum Junction Temperature | | 150 | |

Electrical Characteristics ($T_A=25^{\circ}\text{C}$, Unless Otherwise Noted)

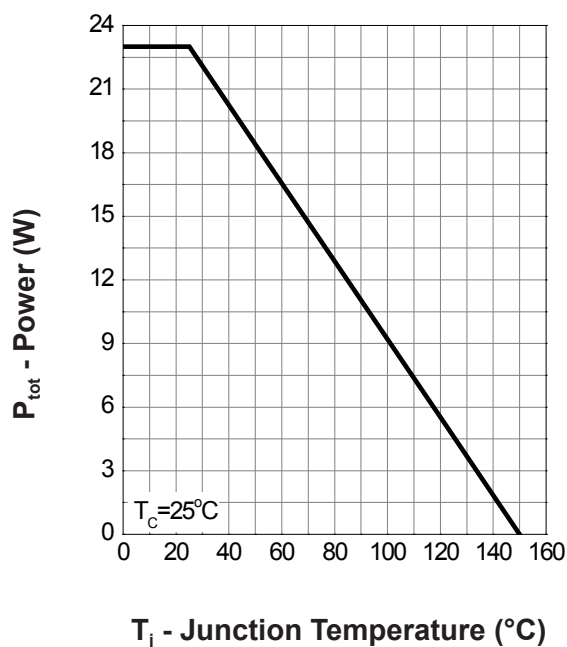
| Symbol | Parameter | Conditions | Min. | Typ. | Max. | Units |
|--|----------------------------------|--|------|----------|-----------|-------|
| Static Characteristics | | | | | | |
| BV _{DSS} | Drain-Source Breakdown Voltage | V _{GS} =0V , I _D =250μA | 100 | --- | --- | V |
| I _{DSS} | Zero Gate Voltage Drain Current | V _{DS} =80V , V _{GS} =0V T _J =85°C | --- | --- | 1.0 30 | μA |
| V _{GS(th)} | Gate Threshold Voltage | V _{GS} =V _{DS} , I _{DS} =250μA | 1.0 | 1.7 | 2.5 | V |
| I _{GSS} | Gate Leakage Current | V _{GS} =±20V , V _{DS} =0V | --- | --- | ±100 | nA |
| R _{DS(ON)} ⁵ | Drain-Source On-state Resistance | V _{GS} =10V , I _D =7A V _{GS} =4.5V , I _D =4A | --- | 70 85 | 82 107 | mΩ |
| Diode Characteristics | | | | | | |
| V _{SD} ⁵ | Diode Forward Voltage | I _{SD} =7A , V _{GS} =0V | --- | 0.8 | 1.3 | V |
| t _{rr} | Reverse Recovery Time | I _{DS} =7A , di _{SD} /dt=100A/μs | --- | 30 | --- | ns |
| Q _{rr} | Reverse Recovery Charge | | --- | 40 | --- | nC |
| Dynamic Characteristics ⁶ | | | | | | |
| R _g | Gate Resistance | V _{GS} =0V , V _{DS} =0V , f=1.0MHz | --- | 1.5 | --- | Ω |
| C _{iss} | Input Capacitance | V _{GS} =0V , V _{DS} =50V , Frequency=1.0MHz | --- | 390 | 510 | pF |
| C _{oss} | Output Capacitance | | --- | 72 | --- | |
| C _{rss} | Reverse Transfer Capacitance | | --- | 20 | --- | |
| T _{d(on)} | Turn-on Delay Time | V _{DD} =30V , R _L =30Ω , I _{DS} =1A , V _{GEN} =10V , R _G =6Ω | --- | 8 | 13 | ns |
| T _r | Turn-on Rise Time | | --- | 6 | 10 | |
| T _{d(off)} | Turn-off Delay Time | | --- | 13 | 21 | |
| T _f | Turn-off Fall Time | | --- | 11 | 18 | |
| Gate Charge Characteristics ⁶ | | | | | | |
| Q _g | Total Gate Charge | V _{DS} =50V , V _{GS} =10V , I _{DS} =7A | --- | 7.5 | 11 | nC |
| Q _{gs} | Gate-Source Charge | | --- | 2.4 | --- | |
| Q _{gd} | Gate-Drain Charge | | --- | 1.6 | --- | |

Note:

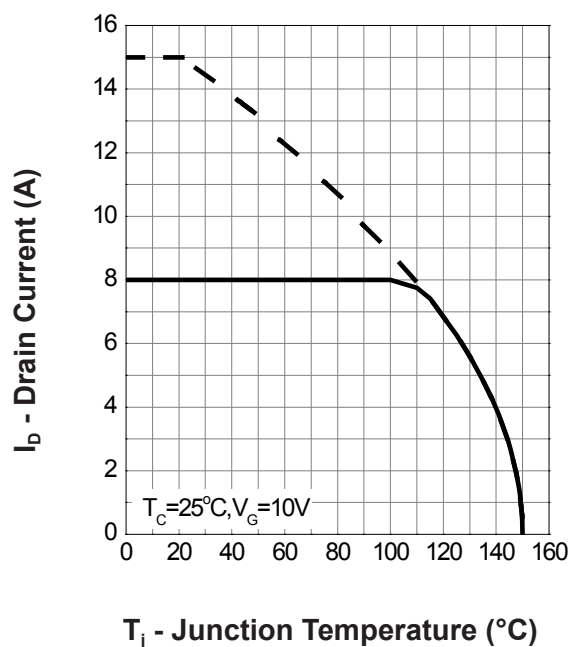
1. Calculated continuous current based on maximum allowable junction temperature. Bonding wire limitation current is 8A.
2. Pulse width limited by max. junction temperature.
3. UIS tested and pulse width limited by maximum junction temperature 150°C (initial temperature $T_J=25^{\circ}\text{C}$).
4. Surface Mounted on 1in^2 pad area.
5. Pulse test ; pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.
6. Guaranteed by design, not subject to production testing.

Typical Characteristics

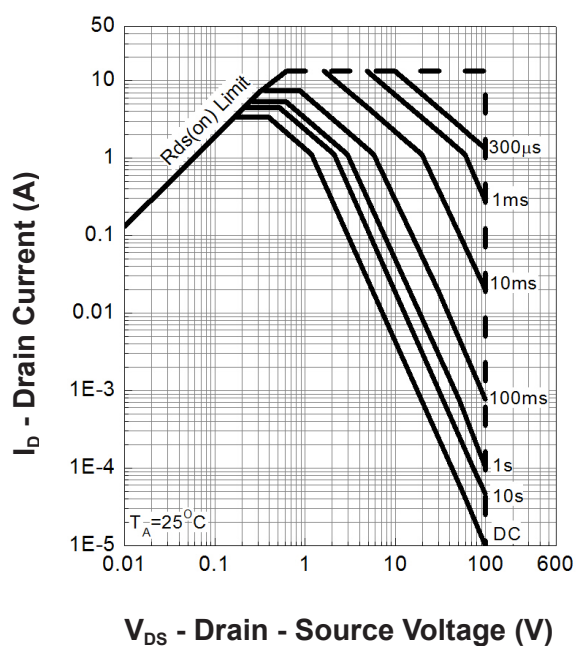
Power Dissipation



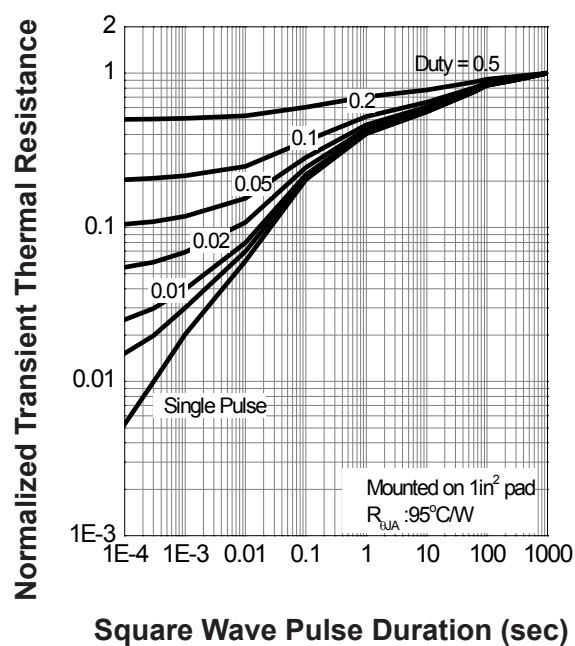
Drain Current



Safe Operation Area

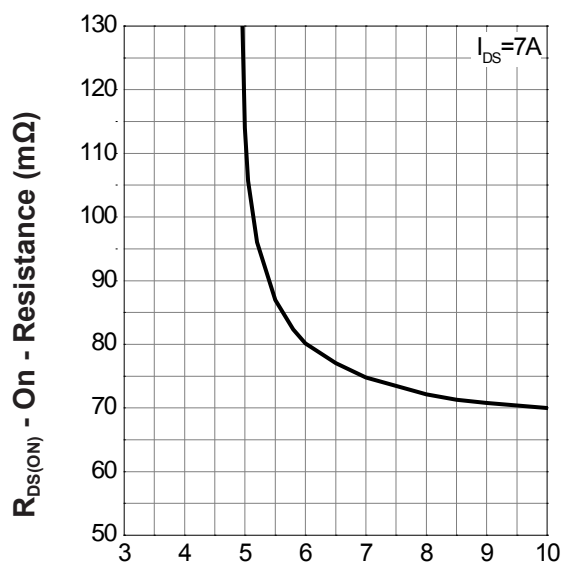


Thermal Transient Impedance



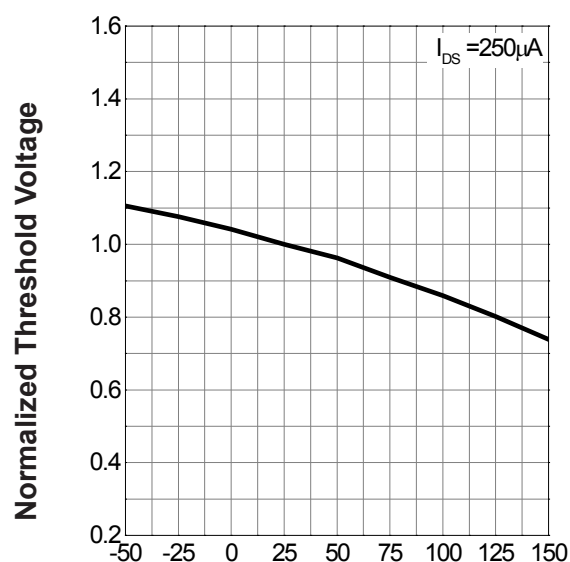
Typical Characteristics (Cont.)

Gate-Source On Resistance



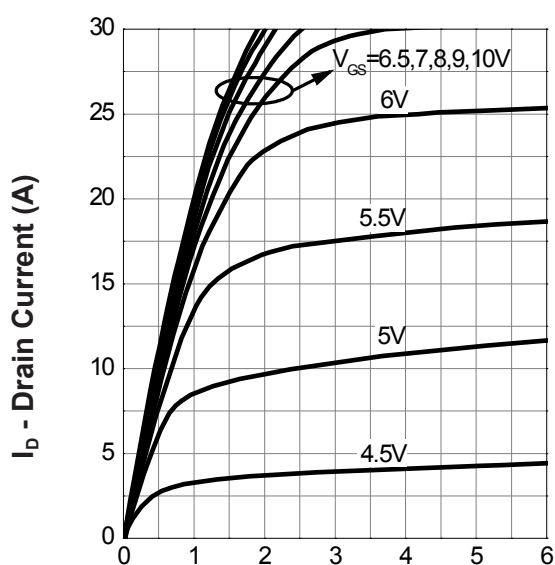
V_{GS} - Gate - Source Voltage (V)

Gate Threshold Voltage



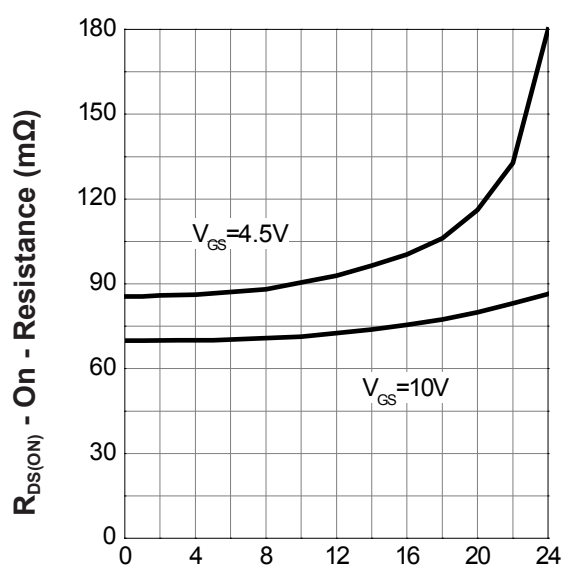
T_J - Junction Temperature ($^{\circ}C$)

Output Characteristics



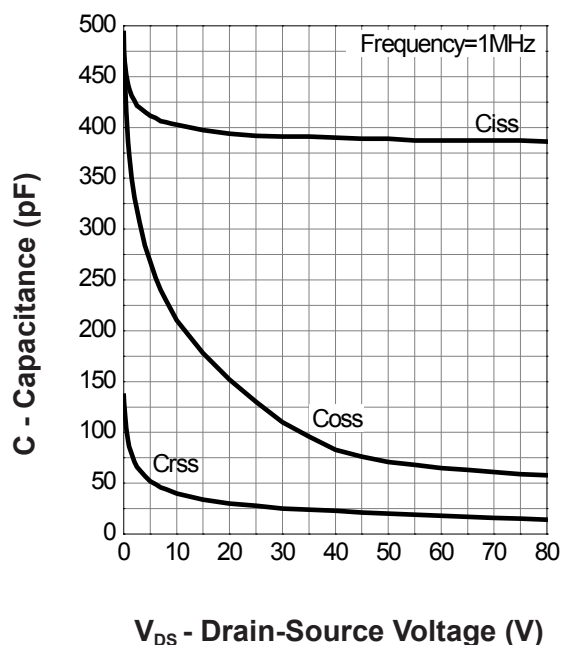
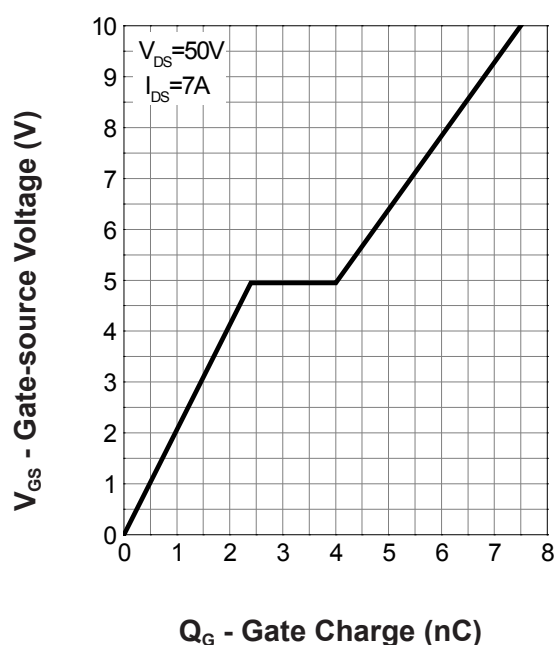
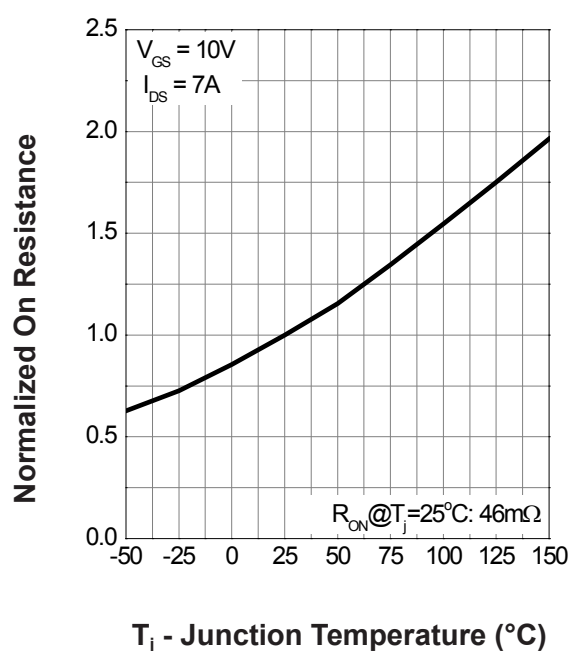
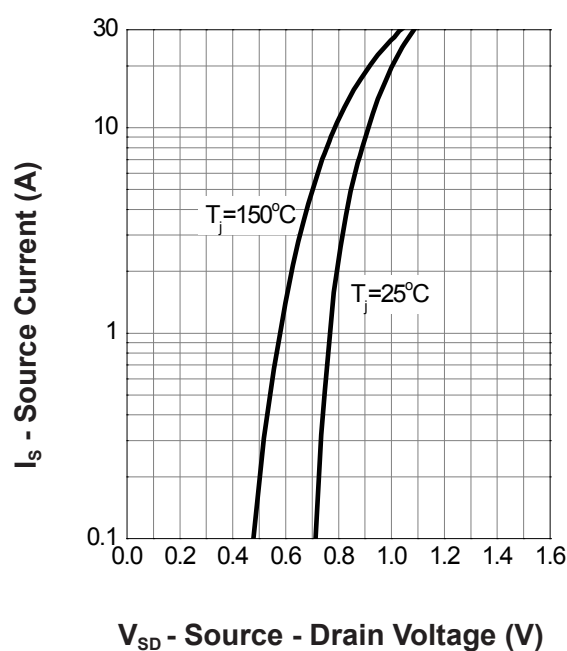
V_{DS} - Drain - Source Voltage (V)

Drain-Source On Resistance

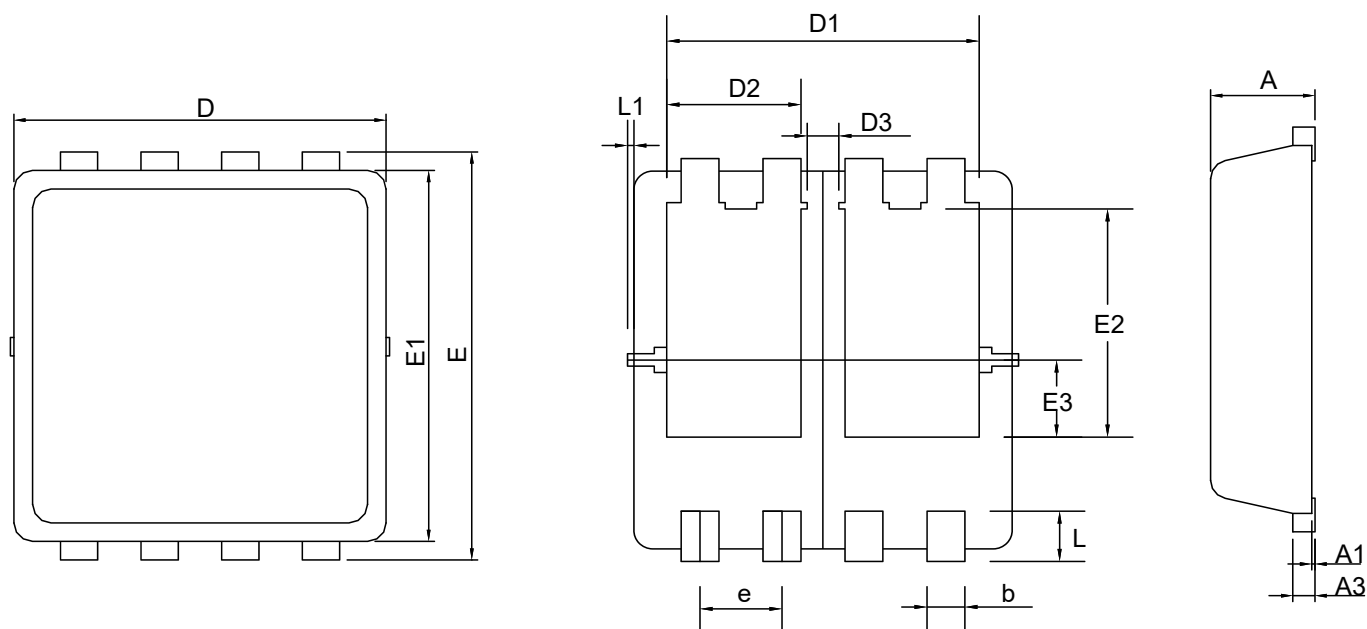


I_D - Drain Current (A)

Typical Characteristics (Cont.)

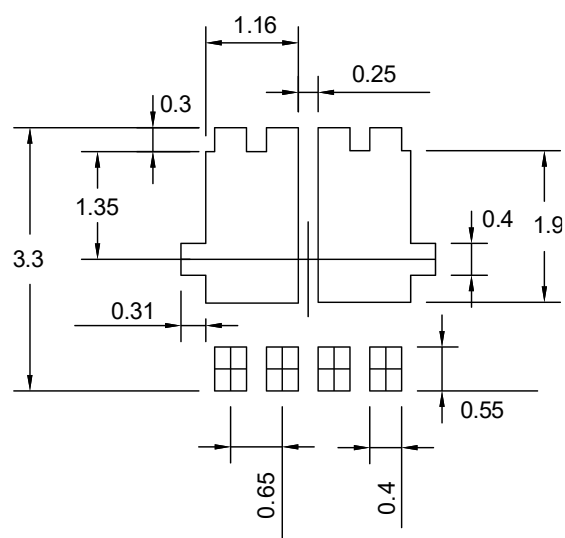
Capacitance

Gate Charge

Drain-Source On Resistance

Source-Drain Diode Forward


Packaging information



| SYMBOL | DFN3X3-8L | | | |
|--------|-------------|-------|-----------|-------|
| | MILLIMETERS | | INCHES | |
| | MIN. | MAX. | MIN. | MAX. |
| A | 0.70 | 0.90 | 0.028 | 0.035 |
| A1 | 0.00 | 0.05 | 0.000 | 0.002 |
| A3 | 0.10 | 0.25 | 0.004 | 0.010 |
| b | 0.24 | 0.35 | 0.009 | 0.014 |
| D | 2.90 | 3.10 | 0.114 | 0.122 |
| D1 | 2.375 | 2.575 | 0.094 | 0.101 |
| D2 | 0.963 | 1.163 | 0.038 | 0.046 |
| D3 | 0.175 | 0.275 | 0.007 | 0.011 |
| E | 3.10 | 3.30 | 0.122 | 0.130 |
| E1 | 2.90 | 3.10 | 0.114 | 0.122 |
| E2 | 1.713 | 1.913 | 0.067 | 0.075 |
| E3 | 0.425 | 0.625 | 0.017 | 0.025 |
| e | 0.65 BSC | | 0.026 BSC | |
| L | 0.30 | 0.50 | 0.012 | 0.020 |
| L1 | 0.000 | 0.100 | 0.000 | 0.004 |

RECOMMENDED LAND PATTERN



UNIT: mm

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