N-Channel MOSFET

General Description

The WSD2018DN22 is the highest performance trench N-Channel MOSFET with extreme high cell density, which provide excellent RDSON and gate charge for most of the small power switching and load switch applications.

The WSD2018DN22 meet the RoHS and Green Product requirement with full function reliability approved.

Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent Cdv/dt effect decline
- Green Device Available

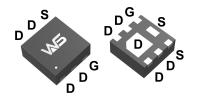
Product Summery

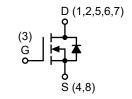
BV _{DSS}	R _{DSON}	I _D
20V	10mΩ	12A

Applications

- High Frequency Point-of-Load Synchronous Small power switching for MB/NB/UMPC/VGA
- Networking DC-DC Power System
- Load Switch

DFN2X2-6S Pin Configuration





Absolute Maximum Ratings

Symbol	Parameter	Rating	Units	
V_{DS}	Drain-Source Voltage	20	V	
V_{GS}	Gate-Source Voltage	±10	V	
I _D @T _C =25℃	Continuous Drain Current, V _{GS} @ 4.5V ¹	12	Α	
I _D @T _C =70℃	Continuous Drain Current, V _{GS} @ 4.5V ¹	10	Α	
I _{DM}	Pulsed Drain Current ²	40	Α	
P _D @T _A =25°C	Total Power Dissipation ³	1.5	W	
T _{STG}	Storage Temperature Range -55 to 150		$^{\circ}$	
T_J	Operating Junction Temperature Range	Operating Junction Temperature Range -55 to 150		

Thermal Data

Symbol	Parameter Typ.		Max.	Unit	
$R_{\theta JA}$	Thermal Resistance Junction-ambient ¹		167	°C/W	
$R_{ heta JC}$	Thermal Resistance Junction-Case ¹		65	°C/W	

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Electrical Characteristics (TJ=25°C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit	
BV _{DSS}	Drain-Source Breakdown Voltage V _{GS} =0V , I _D =250uA		20			V	
$\triangle BV_{DSS}/\triangle T_{J}$	BVDSS Temperature Coefficient	Reference to 25℃, I _D =1mA		0.027		V/°C	
		V _{GS} =4.5V , I _D =5A		10	15		
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =2.5V , I _D =5A		13	18	mΩ	
		V _{GS} =1.8V , I _D =5A		18	30		
V _{GS(th)}	Gate Threshold Voltage	V -V 1 -250A	0.4	0.7	1.0	V	
$\triangle V_{GS(th)}$	V _{GS(th)} Temperature Coefficient	$V_{GS}=V_{DS}$, $I_D=250uA$		2.56		mV/℃	
	Danier Courses I - also as Courses	V _{DS} =16V , V _{GS} =0V , T _J =25℃			1	uA	
I _{DSS}	Drain-Source Leakage Current	V _{DS} =16V , V _{GS} =0V , T _J =55℃			5		
I _{GSS}	Gate-Source Leakage Current	V_{GS} = \pm 12 V , V_{DS} =0 V			±100	nA	
gfs	orward Transconductance	V _{DS} =4V , I _D =9.7A	20			S	
R_g	Gate Resistance	ce f=1MHz		2.5		Ω	
Qg	Total Gate Charge (4.5V)				32		
Q _{gs}	Gate-Source Charge	V_{DS} =4V , V_{GS} =5V , I_{D} =10A		2.5		nC	
Q _{gd}	Gate-Drain Charge			6.5			
T _{d(on)}	Turn-On Delay Time			12	20		
Tr	Rise Time	V_{DD} =4V , V_{GS} =4.5V , R_{G} =1 Ω		10	25	ns	
T _{d(off)}	Turn-Off Delay Time	I _D =10A ,RL=0.4Ω		65	70		
T _f	Fall Time			20	60		
C _{iss}	Input Capacitance			1800			
C _{oss}	Output Capacitance	V _{DS} =4V , V _{GS} =0V , f=1MHz		650		pF	
C _{rss}	Reverse Transfer Capacitance			450			

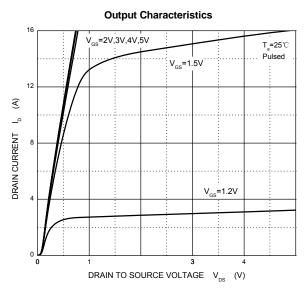
Notes:

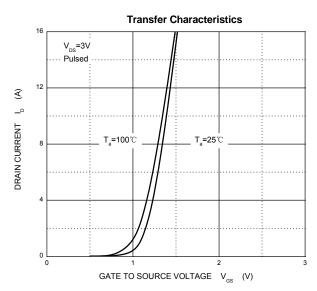
- 1. Surface mounted on FR4 board using 1 square inch pad size,1oz copper.
- 2. Surface mounted on FR4 board using the minimum pad size, 1oz copper.
- 3. Pulse test : Pulse width=300µs, duty cycle≤2%.
- 4. These parameters have no way to verify.

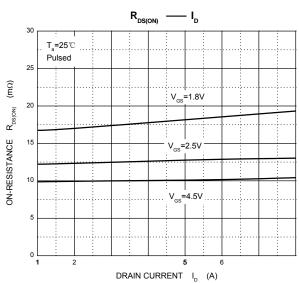


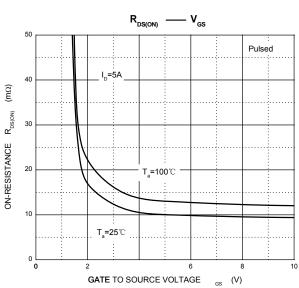


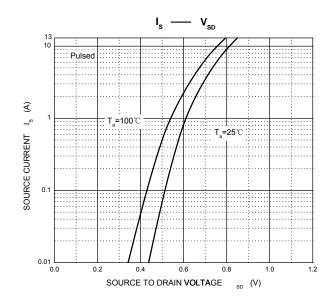
Typical Characteristics

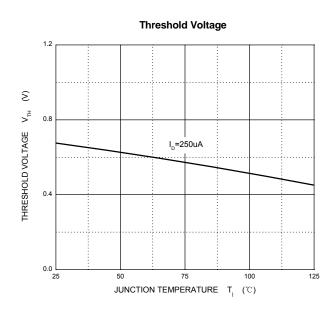






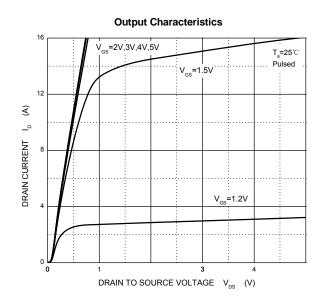


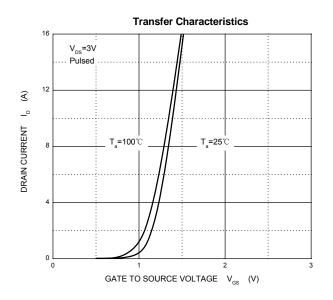


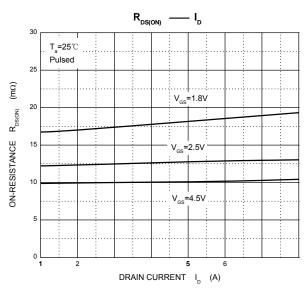


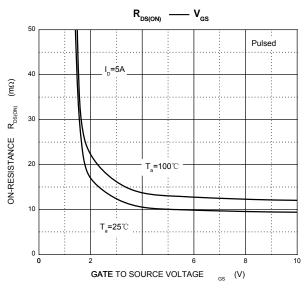


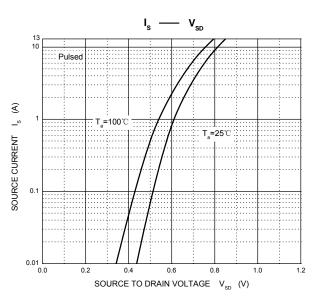
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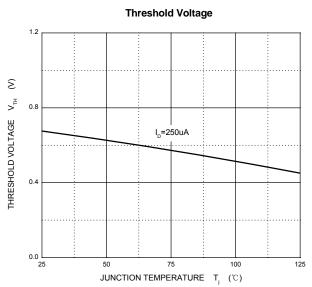






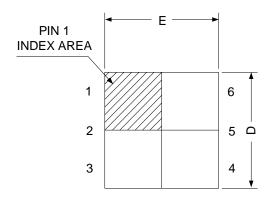


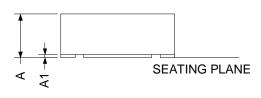


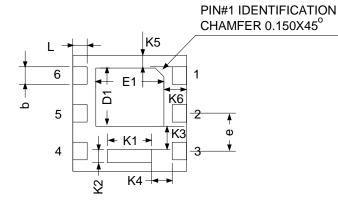


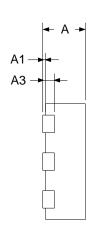


Packaging information



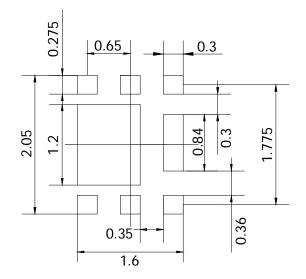






Ş	DFN2X2-6S				
SYMBOL	MILLIMETERS		INCHES		
P	MIN.	MAX.	MIN.	MAX.	
Α	0.70	0.80	0.028	0.031	
A1	0.00	0.05	0.000	0.002	
А3	0.200 REF		0.008	REF	
b	0.25	0.35	0.010	0.014	
D	1.90	2.10	0.075	0.083	
Е	1.90	2.10	0.075	0.083	
D1	0.90	1.10	0.035	0.043	
E1	0.90	1.10	0.035	0.043	
е	0.65 BSC		0.026 BSC		
L	0.20	0.30	0.008	0.012	
K1	0.65	0.85	0.026	0.033	
K2	0.20	-	0.008	-	
K3	0.20	-	0.008	-	
K4	0.32	-	0.013	-	
K5	0.20	0.26	0.008	0.010	
K6	0.45	0.55	0.018	0.022	

RECOMMENDED LAND PATTERN



UNIT: mm



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