

**Dual N-Channel MOSFET** 

# **General Description**

The WSD3810DN33 is the highest performance trench Dual N-Channel MOSFET with extreme high cell density, which provide excellent  $R_{DS(ON)}$  and gate charge for most of the synchronous buck converter applications.

The WSD3810DN33 meet the RoHS and Green Product requirement 100%  $E_{AS}$  guaranteed with full function reliability approved.

## **Features**

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- 100% E<sub>AS</sub> Guaranteed
- Green Device Available

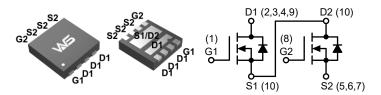
## **Product Summery**

BV <sub>DSS</sub>	R <sub>DS(ON)</sub>	I <sub>D</sub>	I <sub>TEM</sub>
30V	9.0mΩ	18A	Q1
30V	8.5mΩ	18A	Q2

## **Applications**

- High Frequency Point-of-Load Synchronous Buck Converter for MB/NB/UMPC/VGA
- Networking DC-DC Power System
- Load Switch

## **DFN33X33-8S Pin Configuration**



# **Absolute Maximum Ratings** (T<sub>A</sub>=25°C, Unless Otherwise Noted)

Cumbal	Parameter		Rat	Units	
Symbol			Q1	Q2	Units
V <sub>DS</sub>	Drain-Source Voltage		30	30	V
$V_{GS}$	Gate-Source Voltage		±20	±20	V
	Due in Comment (Continued) 13	T <sub>C</sub> =25°C	18	18	
l <sub>D</sub>	Drain Current (Continuous) <sup>1,3</sup>	T <sub>C</sub> =100°C	12.3	12.3	Α
I <sub>DM</sub>	Pulsed Drain Current <sup>2</sup>		45	45	
E <sub>AS</sub>	Single Pulse Avalanche Energy	$V_{DD}$ =25V , $V_{GS}$ =10V , L=1.0mH , $R_{G}$ =25 $\Omega$	11	11	mJ
P <sub>D</sub>	Power Dissipation T <sub>C</sub> =25°C		20	20	W
T <sub>STG</sub>	Storage Temperature Range		-55 to 150	-55 to 150	°C
$T_J$	Operating Junction Temperature Range		-55 to 150	-55 to 150	C

## **Thermal Data**

Symbol	Parameter	Typ. Max.		Units	
$R_{ heta JC}$	Thermal Resistance, Junction-to-Case		6	°C/W	



## **Dual N-Channel MOSFET**

# Q1 Electrical Characteristics (T<sub>A</sub>=25°C, Unless Otherwise Noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units	
Static							
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =250μA	30			V	
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =30V , V <sub>GS</sub> =0V			1.0	μA	
I <sub>GSS</sub>	Gate Leakage Current	V <sub>GS</sub> =±20V , V <sub>DS</sub> =0V			±100	nA	
$V_{GS(th)}$	Gate Threshold Voltage	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>DS</sub> =250μA	1.0	1.6	2.5	V	
D	Drain-Source On-state Resistance	V <sub>GS</sub> =10V , I <sub>D</sub> =10A		9.0	10.8	mΩ	
R <sub>DS(ON)</sub>	Drain-Source On-state Resistance	V <sub>GS</sub> =4.5V , I <sub>D</sub> =8A		12	17.5		
g <sub>fs</sub>	Forward Transconductance	V <sub>DS</sub> =5V , I <sub>D</sub> =5A		12		S	
$V_{SD}$	Diode Forward Voltage	I <sub>SD</sub> =1A, V <sub>GS</sub> =0V			1.3	V	
Switching	Switching						
$Q_g$	Total Gate Charge			8			
$Q_{gs}$	Gate-Source Charge	V <sub>DS</sub> =15V , V <sub>GS</sub> =10V , I <sub>D</sub> =5A		1.6		nC	
$Q_{\mathrm{gd}}$	Gate-Drain Charge			1.2			
T <sub>d(on)</sub>	Turn-on Delay Time			8.5			
T <sub>r</sub>	Turn-on Rise Time	V <sub>DD</sub> =15V , V <sub>GS</sub> =10V ,		10			
$T_{d(off)}$	Turn-off Delay Time	$R_G=6\Omega$ , $I_D=1A$		14		ns	
T <sub>f</sub>	Turn-off Fall Time			10.6			
Dynamic							
C <sub>iss</sub>	Input Capacitance			455			
C <sub>oss</sub>	Output Capacitance	V <sub>DS</sub> =15V , V <sub>GS</sub> =0V , f = 1.0MHz		318		pF	
C <sub>rss</sub>	Reverse Transfer Capacitance			22			

## Note:

<sup>1.</sup> The value of  $R_{\theta JA}$  is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with  $T_A$ =25°C. The value in any given application depends on the user's specific board design.

<sup>2.</sup> Repetitive rating, pulse width limited by junction temperature.

<sup>3.</sup> The current rating is based on the t≤ 10s junction to ambient thermal resistance rating.



**Dual N-Channel MOSFET** 

# Q2 Electrical Characteristics (T<sub>A</sub>=25°C, Unless Otherwise Noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units
Static						
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =250μA	30			V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =30V , V <sub>GS</sub> =0V			1.0	μA
I <sub>GSS</sub>	Gate Leakage Current	V <sub>GS</sub> =±20V , V <sub>DS</sub> =0V			±100	nA
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>DS</sub> =250μA	1.0	1.6	2.5	V
В	Dunin Course On atota Basistana	V <sub>GS</sub> =10V , I <sub>D</sub> =10A		8.5	10.5	· mΩ
R <sub>DS(ON)</sub>	Drain-Source On-state Resistance	V <sub>GS</sub> =4.5V , I <sub>D</sub> =8A		12.5	16	
g <sub>fs</sub>	Forward Transconductance	V <sub>DS</sub> =5V , I <sub>D</sub> =5A		12		S
V <sub>SD</sub>	Diode Forward Voltage	I <sub>SD</sub> =1A, V <sub>GS</sub> =0V			1.3	V
Switching						
$Q_g$	Total Gate Charge			8		
Q <sub>gs</sub>	Gate-Source Charge	V <sub>DS</sub> =15V , V <sub>GS</sub> =10V , I <sub>D</sub> =5A		1.6		nC
Q <sub>gd</sub>	Gate-Drain Charge			1.2		
$T_{d(on)}$	Turn-on Delay Time			8.5		
T <sub>r</sub>	Turn-on Rise Time	V <sub>DD</sub> =15V , V <sub>GS</sub> =10V ,		10		
T <sub>d(off)</sub>	Turn-off Delay Time	$R_G=6\Omega$ , $I_D=1A$		14		ns
T <sub>f</sub>	Turn-off Fall Time			10.6		
Dynamic						
C <sub>iss</sub>	Input Capacitance			455		
C <sub>oss</sub>	Output Capacitance	V <sub>DS</sub> =15V , V <sub>GS</sub> =0V , f = 1.0MHz		318		pF
C <sub>rss</sub>	Reverse Transfer Capacitance			22		

## Note:

<sup>1.</sup> The value of  $R_{\theta JA}$  is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with  $T_A$ =25°C. The value in any given application depends on the user's specific board design.

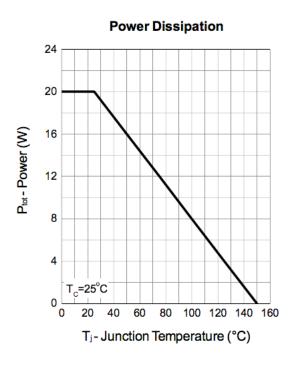
<sup>2.</sup> Repetitive rating, pulse width limited by junction temperature.

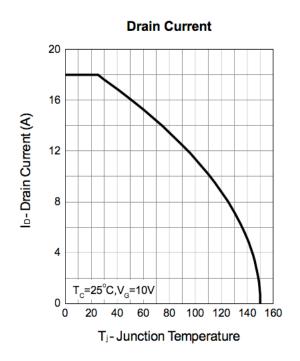
<sup>3.</sup> The current rating is based on the t≤ 10s junction to ambient thermal resistance rating.



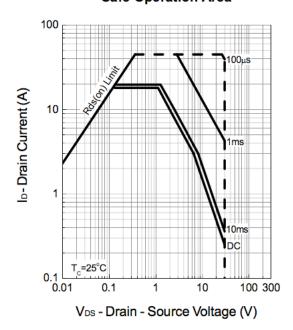


# **Q1 Typical Characteristics**

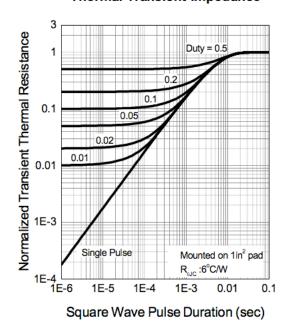




## Safe Operation Area



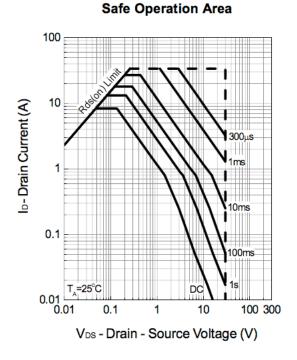
## **Thermal Transient Impedance**



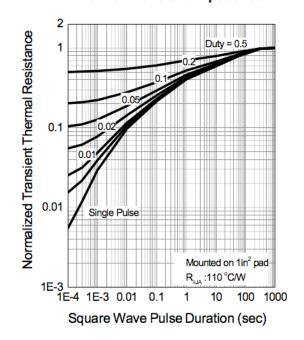




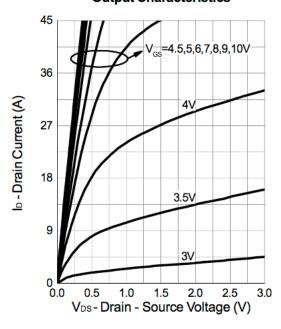
# **Q1 Typical Characteristics (Cont.)**



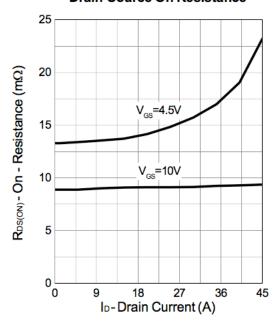
## **Thermal Transient Impedance**



## **Output Characteristics**



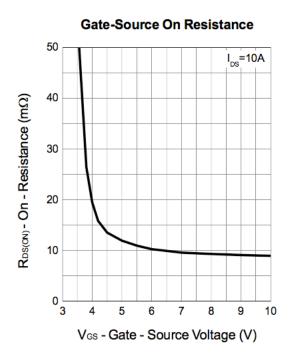
## **Drain-Source On Resistance**





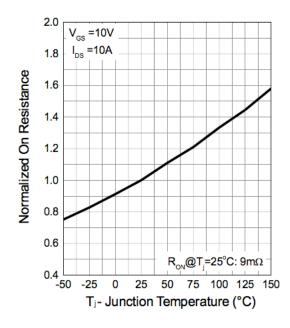


# **Q1 Typical Characteristics (Cont.)**

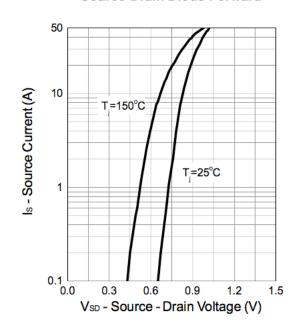


# Gate Threshold Voltage 1.4 9.5 1.2 0.6 0.4 -50 -25 0 25 50 75 100 125 150 Tj.- Junction Temperature (°C)

## **Drain-Source On Resistance**

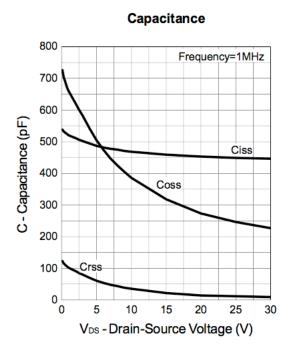


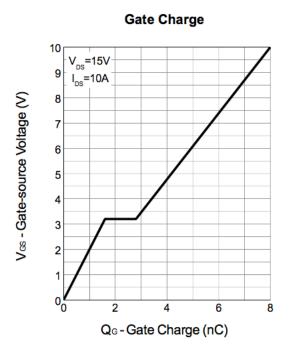
## Source-Drain Diode Forward





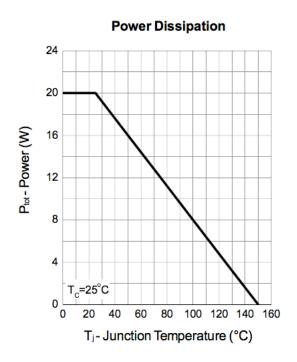
# **Q1 Typical Characteristics (Cont.)**

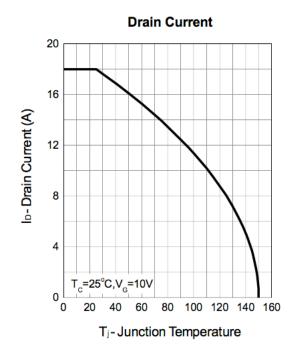




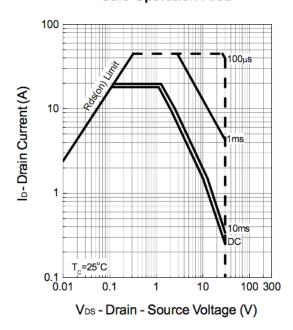


# **Q2 Typical Characteristics**

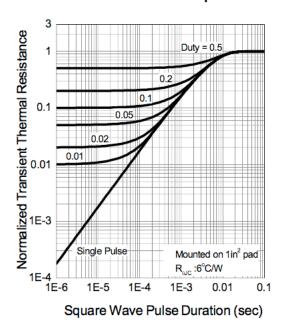




## Safe Operation Area



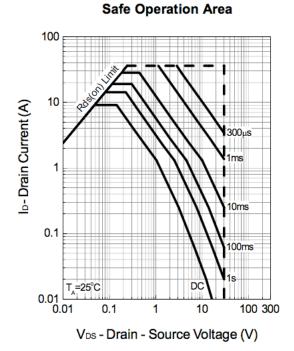
## Thermal Transient Impedance



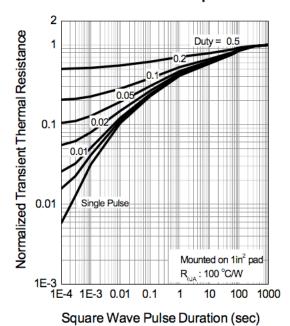




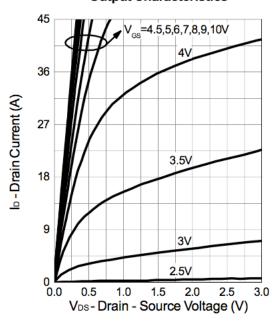
# **Q2 Typical Characteristics (Cont.)**



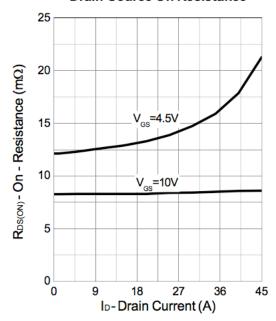
## **Thermal Transient Impedance**



**Output Characteristics** 

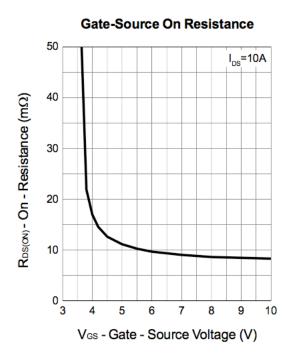


## **Drain-Source On Resistance**



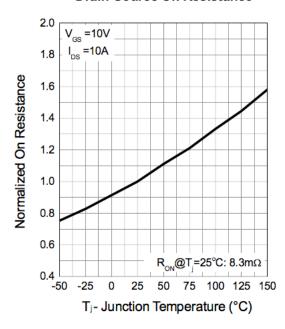


# **Q2 Typical Characteristics (Cont.)**

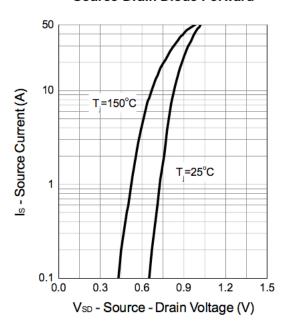


# **Gate Threshold Voltage** 1.4 I<sub>DS</sub>=250μA 1.2 Normalized Threshold Voltage 1.0 8.0 0.6 0.4 <u>-</u> -50 -25 0 25 50 75 100 125 150 T<sub>j</sub> - Junction Temperature (°C)

## **Drain-Source On Resistance**



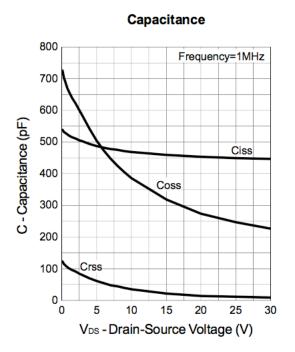
## Source-Drain Diode Forward

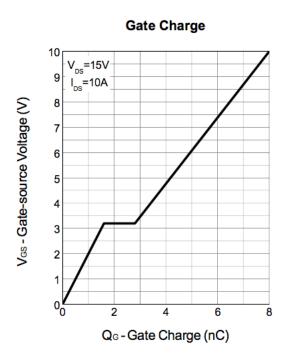






# **Q2 Typical Characteristics (Cont.)**

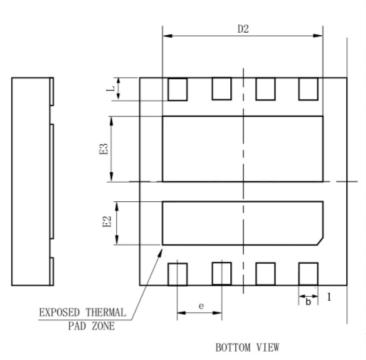








# **Packaging information**



SYMBOL	MILLIMETER				
SYMBOL	MIN	MID	MAX		
A	0.70	0.75	0.80		
A1		0.02	0.05		
b	0. 25	0.30	0.35		
С	0. 18	0.20	0. 25		
D	3. 20	3. 30	3. 40		
<b>D</b> 2	2. 50	2.60	2. 70		
Е	3. 20	3. 30	3.40		
E2	0.65	0.70	0.75		
E3	0. 95	1.05	1. 15		
e	0. 70 BSC				
L	0.30	0.35	0.40		



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