

N-Channel MOSFET

General Description

The WSD4032DN33 is the highest performance trench N-Channel MOSFET with extreme high cell density, which provide excellent $R_{DS(ON)}$ and gate charge for most of the synchronous buck converter applications.

The WSD4032DN33 meet the RoHS and Green Product requirement 100% E_{AS} guaranteed with full function reliability approved.

Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- 100% E_{AS} Guaranteed
- Green Device Available

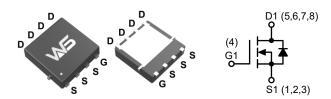
Product Summery

BV _{DSS}	R _{DS(ON)}	I _D	
40V	13.5mΩ	25A	

Applications

- High Frequency Point-of-Load Synchronous Buck Converter for MB/NB/UMPC/VGA
- Networking DC-DC Power System
- Load Switch

DFN3X3-8L Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	40	V
V _{GS}	Gate-Source Voltage	±20	V
I _D @T _C =25°C	Continuous Drain Current, V _{GS} @ 10V ¹	25	
I _D @T _C =70°C	Continuous Drain Current, V _{GS} @ 10V ¹	20	А
I _{DM}	Pulsed Drain Current ²	75	
E _{AS}	E _{AS} Single Pulse Avalanche Energy ³		mJ
I _{AS}	Avalanche Current ³	30	А
P _D @T _A =25°C Total Power Dissipation ⁴		1.3	W
T _{STG}	Storage Temperature Range -55 to 150		°C
TJ	Operating Junction Temperature Range	-55 to 150	

Thermal Data

Symbol	Parameter	Тур.	Max.	Units	
$R_{ heta JA}$	Thermal Resistance, Junction-to-Ambient ¹		65	°C/M	
$R_{ heta JC}$	Thermal Resistance, Junction-to-Case ¹		20	°C/W	



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Electrical Characteristics (T_J=25°C, Unless Otherwise Noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =250μA	40			V
$\Delta BV_{DSS}/\Delta T_{J}$	BV _{DSS} Temperature Coefficient	Reference to 25°C, I _D =1mA		0.024		V/°C
В	Statis Danier Course On Basistan 2	V _{GS} =10V , I _D =12A		13.5	16.5	0
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =4.5V , I _D =10A		19	24.5	mΩ
V _{GS(th)}	Gate Threshold Voltage	\\ _\\	1.5	1.8	2.5	V
$\Delta V_{GS(th)}$	V _{GS(th)} Temperature Coefficient	- V _{GS} =V _{DS} , I _D =250μA		-5.07		mV/°C
	Drain Sauras Laskaga Current	V _{DS} =24V , V _{GS} =0V , T _J =25°C			1.0	
I _{DSS}	Drain-Source Leakage Current	V _{DS} =24V , V _{GS} =0V , T _J =55°C			30	μA
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±20V , V _{DS} =0V			±100	nA
9 _{fs}	Forward Transconductance	V _{DS} =5V , I _D =8A		31		S
R_g	Gate Resistance	V _{DS} =0V , V _{GS} =0V , f = 1.0MHz		1.1	1.8	Ω
Qg	Total Gate Charge (4.5V)			9.4		
Q_{gs}	Gate-Source Charge	V _{DS} =15V , V _{GS} =4.5V , I _D =12A		3.9		nC
Q_gd	Gate-Drain Charge			3.0		
T _{d(on)}	Turn-On Delay Time			12	14	
T _r	Rise Time	V _{DD} =15V,V _{GEN} =10V,		10	17	
T _{d(off)}	Turn-Off Delay Time	$R_G=6\Omega$, $I_D=1A$, $R_L=15\Omega$		23	42	ns
T _f	Fall Time			6	12	
C _{iss}	Input Capacitance			1125	1200	
C _{oss}	Output Capacitance	V _{DS} =15V , V _{GS} =0V , f = 1.0MHz		132	183	pF
C _{rss}	Reverse Transfer Capacitance			70	110	

Guaranteed Avalanche Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units
E _{AS}	Single Pulse Avalanche Energy ⁵	V _{DD} =25V , L=0.1mH , I _{AS} =23A		26		mJ

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units
I _S	Continuous Source Current 1,6	V =V =OV Force Current			8	Α
I _{SM}	Pulsed Source Curren ^{2,6}	V _G =V _D =0V , Force Current			38	Α
V_{SD}	Diode Forward Voltage ²	V _{GS} =0V , I _S =1A , T _J =25°C			1.1	V
t _{rr}	Reverse Recovery Time	L =12A dl/dt=100A/up T =25°C		15		ns
Q_{rr}	Reverse Recovery Charge	I _F =12A, dI/dt=100A/µs,T _J =25°C		9.5		nC

Note:

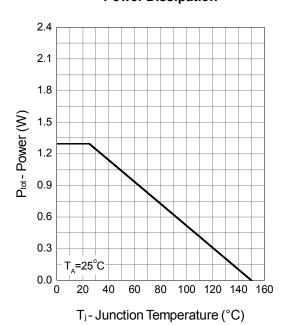
- 1. The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper, t<10sec.
- 2. The data tested by pulsed , pulse width $\leq 300 \mu s$, duty cycle $\leq 2\%$
- 3. The E $_{\rm AS}$ data shows Max. rating . The test condition is $\rm\,V_{DD}$ =25V, $\rm\,V_{GS}$ =10V, L=0.1mH, I $_{\rm AS}$ =23A
- 4. The power dissipation is limited by 150°C junction temperature.
- 5. The Min. value is 100% E_{AS} tested guarantee.
- 6. The data is theoretically the same as I_D and I_{DM} , in real applications, should be limited by total power dissipation.



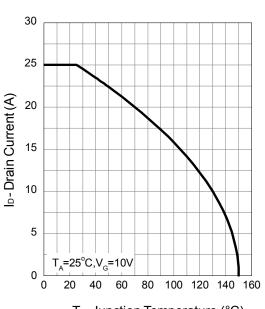


Typical Characteristics

Power Dissipation

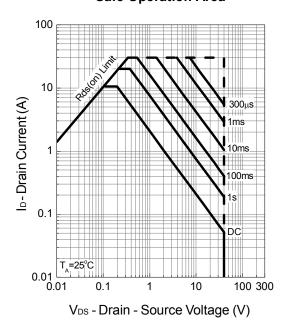


Drain Current

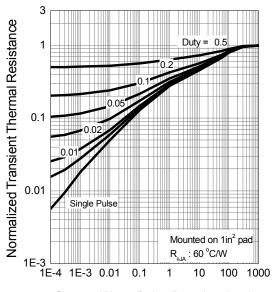


T_j- Junction Temperature (°C)

Safe Operation Area



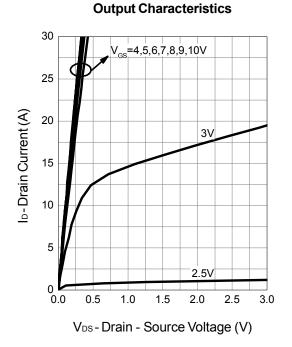
Thermal Transient Impedance



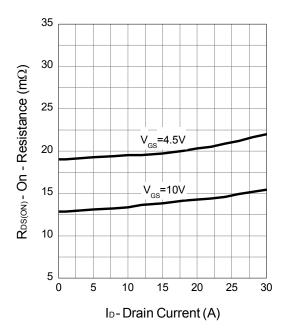
Square Wave Pulse Duration (sec)



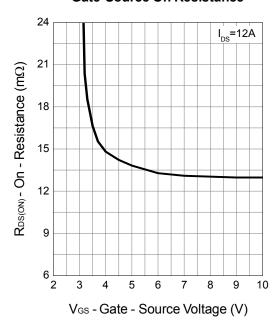
Typical Characteristics (Cont.)



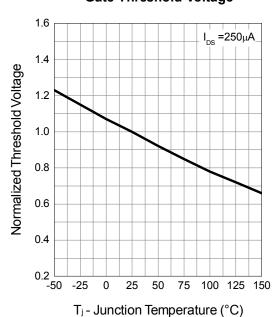
Drain-Source On Resistance



Gate-Source On Resistance



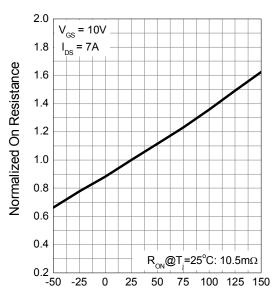
Gate Threshold Voltage





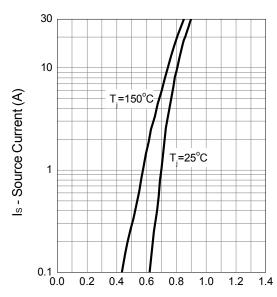
Typical Characteristics (Cont.)

Drain-Source On Resistance



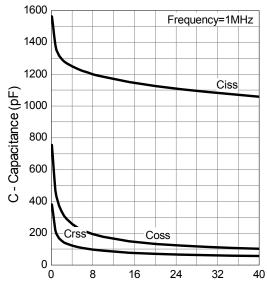
T_j- Junction Temperature (°C)

Source-Drain Diode Forward



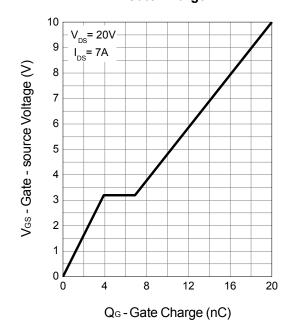
VsD - Source - Drain Voltage (V)

Capacitance



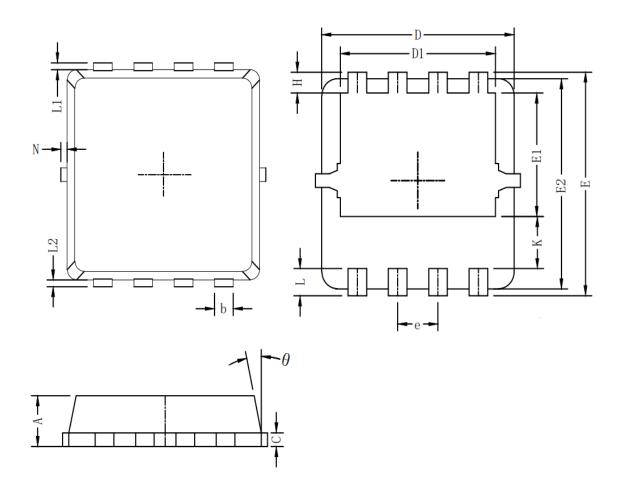
V_{DS} - Drain - Source Voltage (V)

Gate Charge





Packaging information



Symbol	Dim in mm				
Symbol	min	typ	max		
A	0.6	0.75	0.9		
b	0.2	0.3	0.4		
С	0.15	0.2	0.25		
D	3	3.1	3.2		
D1	2.3	2.45	2.6		
E	3.15	3.3	3.45		
E1	1.43	1.73	1.93		
E2	2.9	3.05	3.2		
е		0.65BSC			
Н	0.2	0.35	0.5		
K	0.57	0.77	0.87		
L	0.3	0.4	0.5		
L1/L2	0.1REF				
θ	8°	10°	13°		
N	0		0.15		



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