

## General Description

WSD8823DN22 combines a P-Channel enhancement mode power MOSFET which is produced with high cell density and DMOS trench technology and a low forward voltage schottky diode. the tiny and thin outline saves PCB consumption.

## Applications

- Bidirectional blocking switch;
- DC-DC conversion applications;
- Li-battery charging;

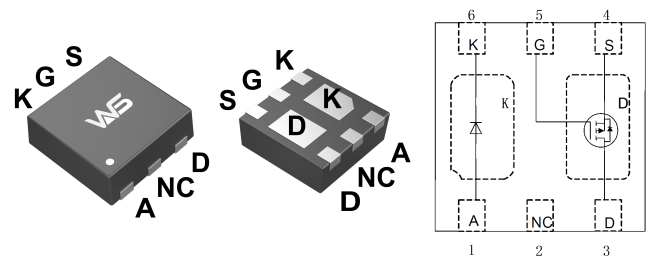
## Product Summery

$BV_{DSS}$	$R_{DS(on)}$	$I_D$
-20V	60mΩ	-3.4A

## Schottky

$V_R$	$V_F$	$I_o$
20V	410mV	2A

## DFN2X2-6S Pin Configuration



5 Vgc`i hY'AU ]a i a 'FUhb[ gAC, AK »Ô,AV |^••Á©!, ã^Á[ c^ãD

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	-20	V
$V_{GS}$	Gate-Source Voltage	$\pm 8$	V
$I_D@T_c=25^\circ C$	Continuous Drain Current, $V_{GS} = -4.5V^1$	-3.4	A
$I_{DM}$	300μS Pulsed Drain Current, ( $V_{GS} = -4.5V$ )	-25	A
$V_R$	Schottky Reverse Voltage	20	V
$I_F$	Schottky Continuous Forw ard Current	2	A
$P_D$	Power Dissipation Derating above $T_A = 25^\circ C$ (Note 2)	1.2	W
$T_{STG}, T_J$	Storage Temperature Range	-55 to 150	$^\circ C$
$R_{\theta JA}$	Thermal Resistance Junction-ambient <sup>1</sup>	80	$^\circ C/W$
$R_{\theta JC}$	Thermal Resistance Junction-Case <sup>1</sup>	50	$^\circ C/W$

Note1: Devices mounted on FR4 PCB with minima soldering pad;

Note2: For a single chip.

## Electrical Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=-250\mu A$	-20	---	---	V
$\Delta BV_{DSS}/\Delta T_J$	BVDSS Temperature Coefficient	Reference to 25°C, $I_D=-1mA$	---	-0.01	---	V/°C
$R_{DS(ON)}$	Static Drain-Source On-Resistance <sup>2</sup>	$V_{GS}=-4.5V, I_D=-1A$	---	60	99	mΩ
		$V_{GS}=-2.5V, I_D=-1A$	---	75	120	
		$V_{GS}=-1.8V, I_D=-1A$	---	105	180	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}, I_D=-250\mu A$	-0.5	-0.7	-1.2	V
$\Delta V_{GS(th)}$	$V_{GS(th)}$ Temperature Coefficient		---	3.13	---	mV/°C
$I_{DSS}$	Drain-Source Leakage Current	$V_{DS}=-16V, V_{GS}=0V, T_J=25^\circ C$	---	---	-1	μA
		$V_{DS}=-16V, V_{GS}=0V, T_J=55^\circ C$	---	---	-5	
$I_{GSS}$	Gate-Source Leakage Current	$V_{GS}=\pm 12V, V_{DS}=0V$	---	---	±100	nA
$g_{fs}$	Forward Transconductance	$V_{DS}=-5V, I_D=-1A$	---	16	---	S
$R_g$	Gate Resistance	$V_{DS}=0V, V_{GS}=0V, f=1MHz$	---	2	---	Ω
$Q_g$	Total Gate Charge (-4.5V)	$V_{DS}=-10V, V_{GS}=-4.5V, I_D=-1A$	---	5.2	---	nC
$Q_{gs}$	Gate-Source Charge		---	0.7	---	
$Q_{gd}$	Gate-Drain Charge		---	1.8	---	
$T_{d(on)}$	Turn-On Delay Time	$V_{DD}=-10V, V_{GS}=-4.5V, R_G=6\Omega, I_D=-1A,$	---	20	---	ns
$T_r$	Rise Time		---	18	---	
$T_{d(off)}$	Turn-Off Delay Time		---	300	---	
$T_f$	Fall Time		---	120	---	
$C_{iss}$	Input Capacitance	$V_{DS}=-10V, V_{GS}=0V, f=1MHz$	---	420	---	pF
$C_{oss}$	Output Capacitance		---	180	---	
$C_{rss}$	Reverse Transfer Capacitance		---	90	---	

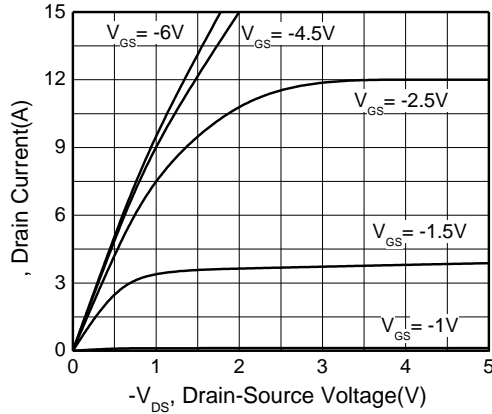
## Schottky Diode

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$V_F$	Forward Voltage Drop	$I_F=1A$	---	410	450	mV
$I_R$	Maximum reverse leakage current	$V_R=20V$	---	15	200	μA

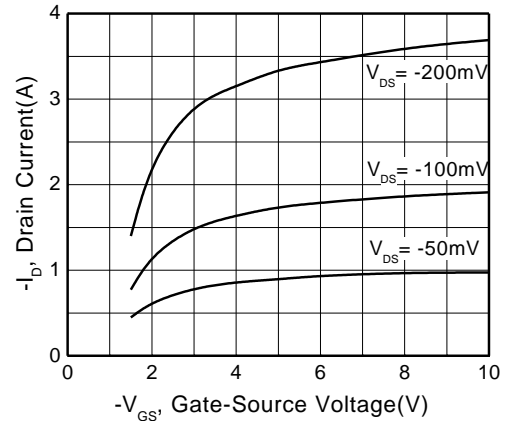
Note :

- 1.The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 20Z copper,  $t \leq 10sec$ .
- 2.The data tested by pulsed , pulse width  $\leq 300\mu s$  , duty cycle  $\leq 2\%$
- 3.The power dissipation is limited by 150°C junction temperature
- 4.The data is theoretically the same as  $I_D$  and  $I_{DM}$  , in real applications , should be limited by total power dissipation.

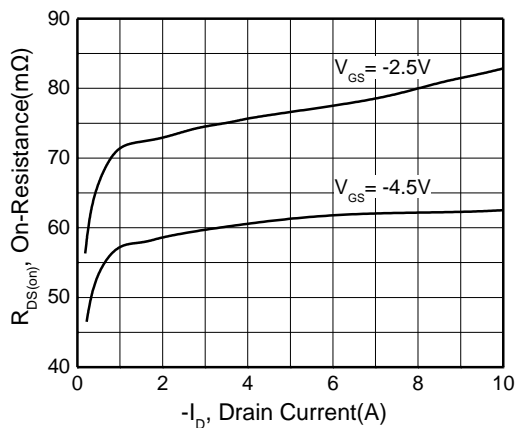
## Typical Characteristics



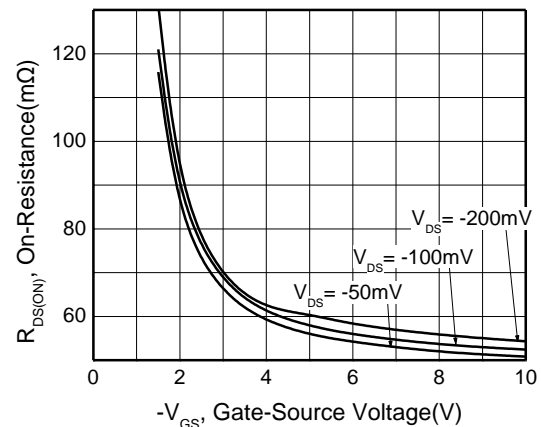
**Fig 1. Output Characteristics**



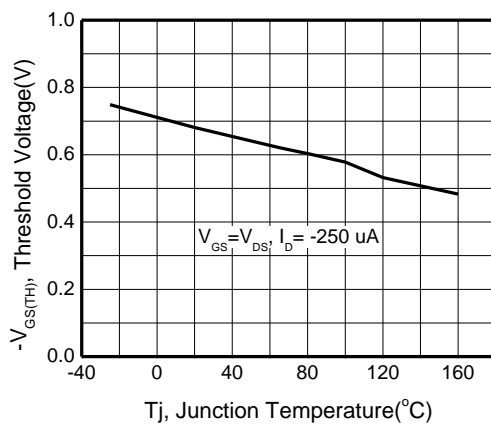
**Fig 2. Transfer Characteristics**



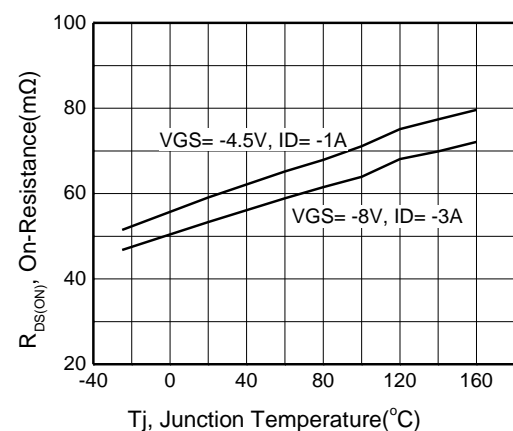
**Fig 3. On-Resistance vs. Drain Current**



**Fig 4. On-Resistance vs. Gate-Source Voltage**

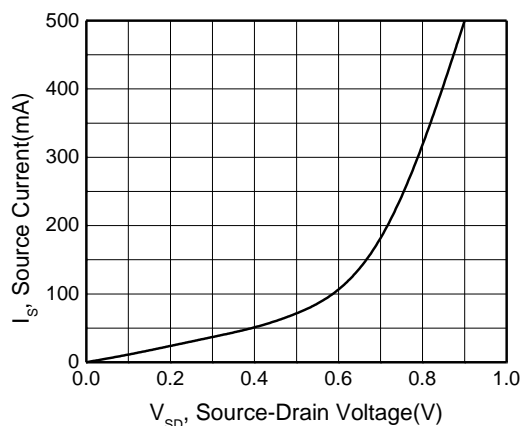


**Fig 5. Threshold Voltage**

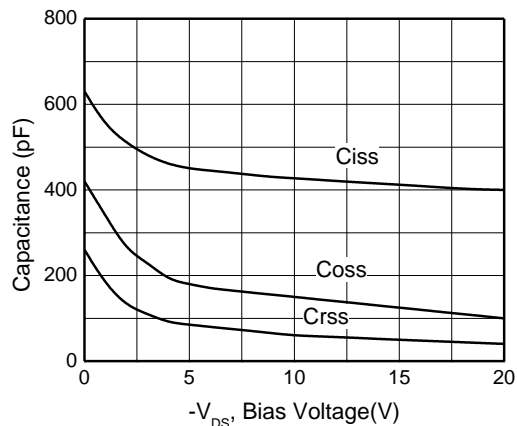


**Fig 6. On-Resistance Temperature Coefficient**

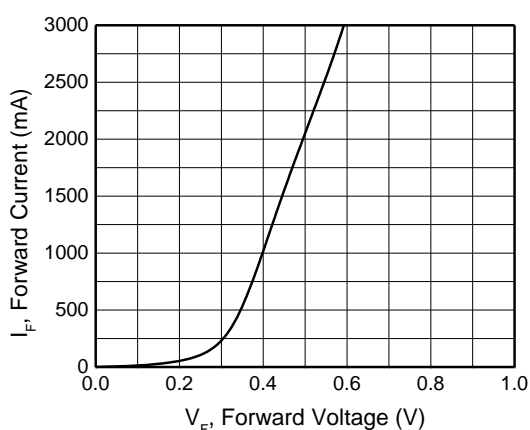
## Typical Characteristics (Cont.)



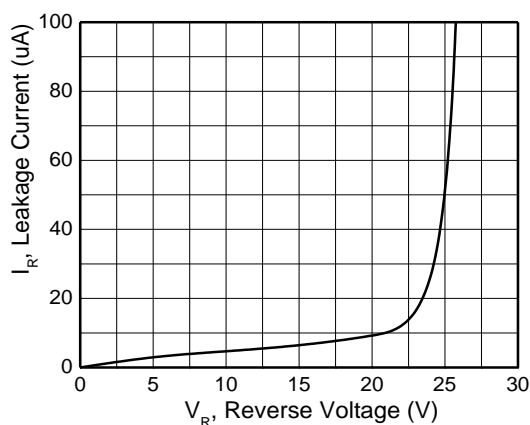
**Fig 7. Body Diode Forward Characteristics**



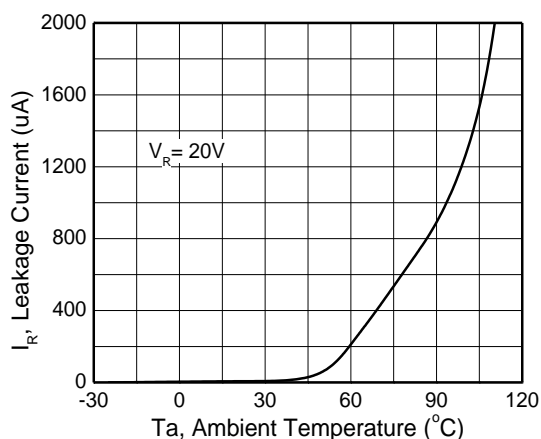
**Fig 8. Capacitance**



**Figure 9. Schottky Forward Characteristics**

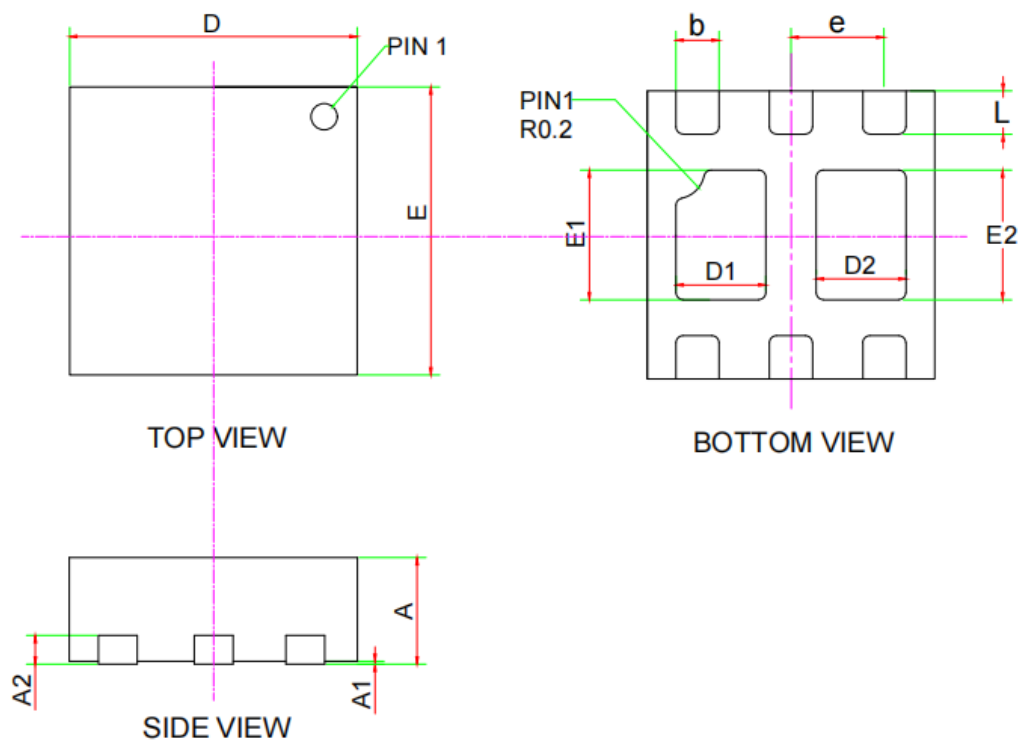


**Figure10. Schottky Reverse Characteristics**



**Figure 11. Leakage Current Vs. Temperature**

## Packaging information



SYMBOL	MIN	NOM	MAX
A	0.70	0.75	0.80
A1	0.00	0.02	0.05
A2	0.18	0.20	0.25
D	1.95	2.00	2.05
E	1.95	2.00	2.05
b	0.25	0.30	0.35
L	0.25	0.30	0.35
D1	0.475	0.625	0.725
E1	0.75	0.90	1.00
D2	0.475	0.625	0.725
E2	0.75	0.90	1.00
R	0.20 REF		
e	0.65 BSC		

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