

General Description

The WSP06N10L is the highest performance trench N-Ch MOSFET with extreme high cell density, which provide excellent R_{DSON} and gate charge for most of the synchronous buck converter applications .

The WSP06N10L meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent Cdv/dt effect decline
- Green Device Available

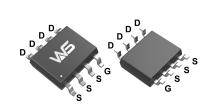
Product Summery

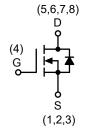
BV _{DSS}	R _{DSON} I _D	
100V	58mΩ	6.0A

Applications

- High Frequency Point-of-Load Synchronous s Small power switching for MB/NB/UMPC/VGA
- Networking DC-DC Power System
- Load Switch

SOP-8L Pin Configuration





Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	100	V
V_{GS}	Gate-Source Voltage	±20	V
I _D @T _A =25℃	Continuous Drain Current, V _{GS} @ 10V ¹	6.0	А
I _D @T _A =70℃	Continuous Drain Current, V _{GS} @ 10V ¹	4.0	А
I _{DM}	Pulsed Drain Current ²	20	А
EAS	Single Pulse Avalanche Energy ³	25	mJ
I _{AS}	Avalanche Current	10	А
P _D @T _A =25℃	Total Power Dissipation ³	3.5	W
T _{STG}	Storage Temperature Range	-55 to 150	$^{\circ}$
TJ	Operating Junction Temperature Range	-55 to 150	°C

Thermal Data

Symbol	Parameter		Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-ambient ¹		70	°C/W
$R_{ heta JA}$	Thermal Resistance Junction-ambient(t <10s) 1		35	°C/W



Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V_{GS} =0 V , I_D =250 u A	100			V
$\triangle BV_{DSS}/\triangle T_{J}$	BVDSS Temperature Coefficient	Reference to 25 $^{\circ}$ C , I _D =1mA		0.098		V/°C
В	Static Drain-Source On-Resistance ²	V_{GS} =10V , I_D =4A		58	70	mΩ
$R_{DS(ON)}$	Static Drain-Source On-Resistance	V_{GS} =4.5 V , I_D =3 A		61	80	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	\/ -\/ -250\	1.2	2	2.5	V
$\triangle V_{GS(th)}$	V _{GS(th)} Temperature Coefficient	$V_{GS}=V_{DS}$, $I_D=250uA$		-4.57		mV/℃
	Drain Source Loakage Current	V_{DS} =80V , V_{GS} =0V , T_J =25 $^{\circ}$ C	80V , V _{GS} =0V , T _J =25℃		1	
I _{DSS}	Drain-Source Leakage Current	V_{DS} =80V , V_{GS} =0V , T_J =55 $^{\circ}$ C			5	· uA
I _{GSS}	Gate-Source Leakage Current	V_{GS} = $\pm 20V$, V_{DS} = $0V$			±100	nA
gfs	Forward Transconductance	V_{DS} =5 V , I_{D} =2 A		20		S
Rg	Gate Resistance	V _{DS} =0V , V _{GS} =0V , f=1MHz		2.5	4	Ω
Qg	Total Gate Charge (10V)			20		
Q_{gs}	Gate-Source Charge	V_{DS} =50V , V_{GS} =10V , I_{D} =4A		4		nC
Q_{gd}	Gate-Drain Charge			4		
T _{d(on)}	Turn-On Delay Time			7		
Tr	Rise Time	V_{DD} =30V , V_{GS} =10V , R_G =6 Ω		12		no
T _{d(off)}	Turn-Off Delay Time	I _D =1A, R _L =30Ω		7		ns
T _f	Fall Time			30		
Ciss	Input Capacitance			920		
C _{oss}	Output Capacitance	V _{DS} =30V , V _{GS} =0V , f=1MHz		60		pF
C _{rss}	Reverse Transfer Capacitance			31		

Guaranteed Avalanche Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
EAS	Single Pulse Avalanche Energy ⁵	V _{DD} =25V , L=0.1mH , I _{AS} =10A	10			mJ

Diode Characteristics

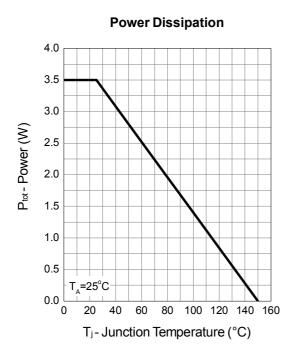
Symbol	Parameter Conditions		Min.	Тур.	Max.	Unit
I _S	Continuous Source Current ^{1,6}	V =V =0V Force Current			3	Α
I _{SM}	Pulsed Source Current ^{2,6}	V _G =V _D =0V , Force Current			14	Α
V_{SD}	Diode Forward Voltage ²	V_{GS} =0V , I_{S} =3A , T_{J} =25 $^{\circ}$ C			1.2	V
t _{rr}	Reverse Recovery Time	 - IF=3A,dI/dt=100A/µs,TJ=25℃		29		nS
Q _{rr}	Reverse Recovery Charge			42		nC

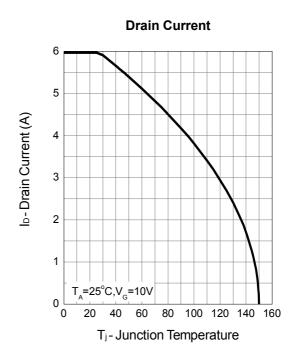
Note:

- 1. The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper, t<10sec.
- 2.The data tested by pulsed , pulse width \leq 300us , duty cycle \leq 2%
- 3.The EAS data shows Max. rating . The test condition is V_{DD} =25V, V_{GS} =10V,L=0.1mH, I_{AS} =10A
- 5.The Min. value is 100% EAS tested guarantee.
- 6. The data is theoretically the same as I_D and I_{DM} , in real applications, should be limited by total power dissipation.

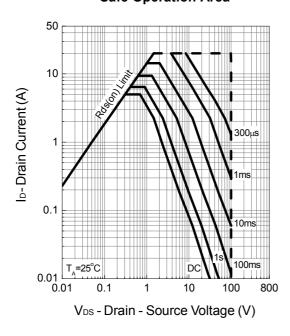


Typical Operating Characteristics

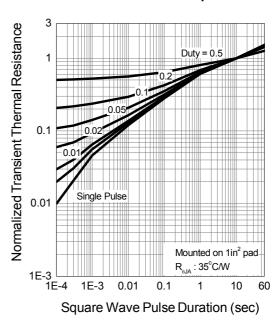




Safe Operation Area

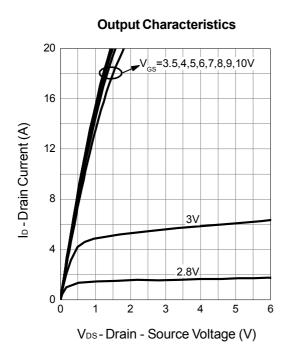


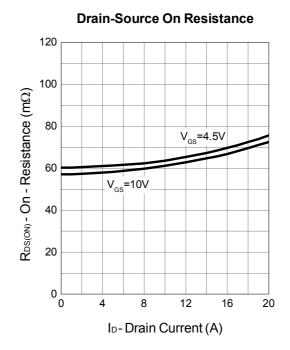
Thermal Transient Impedance

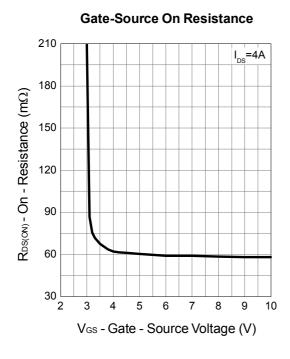


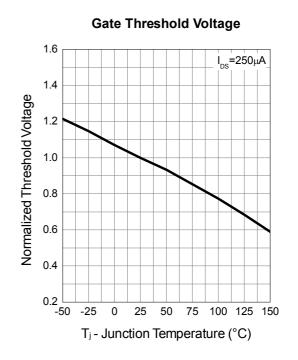


Typical Operating Characteristics





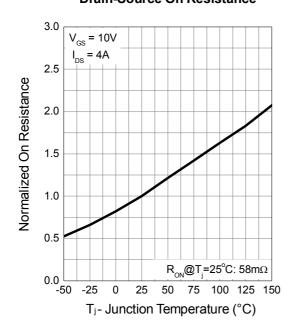




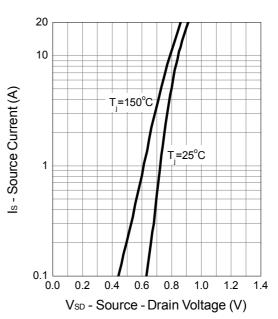


Typical Operating Characteristics

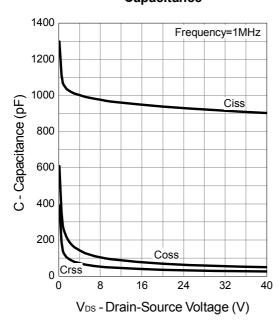
Drain-Source On Resistance



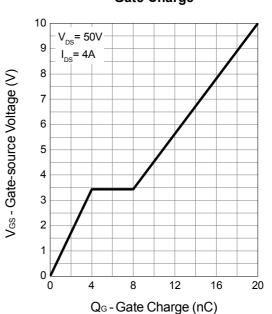
Source-Drain Diode Forward



Capacitance

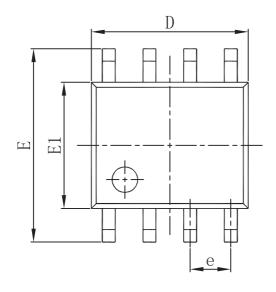


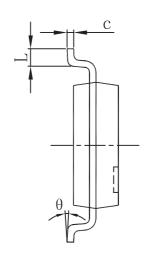
Gate Charge

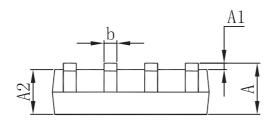




Packaging information







Comple al	Dimensions In Millimeters		Dimensions In Inches		
Symbol	Min	Max	Min	Max	
A	1.350	1.750	0.053	0.069	
A1	0. 100	0. 250	0.004	0.010	
A2	1. 350	1.550	0. 053	0.061	
ь	0. 330	0. 510	0. 013	0. 020	
С	0. 170	0. 250	0.007	0.010	
D	4. 800	5. 000	0. 189	0. 197	
e	1.270 (BSC)		0.050 (BSC)		
Е	5. 800	6. 200	0. 228	0. 244	
E1	3. 800	4.000	0. 150	0. 157	
L	0.400	1. 270	0. 016	0.050	
θ	0°	8°	0°	8°	



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