



#### **General Description**

The WSP4077is the highest performance trench P-Ch MOSFET with extreme high cell density , which provide excellent  $R_{\text{DSON}}$  and gate charge for most of the synchronous buck converter applications .

The WSP4077 meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

#### **Features**

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- 100% EAS Guaranteed
- Green Device Available

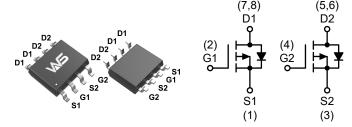
#### **Product Summery**

BV <sub>DSS</sub>	R <sub>DSON</sub>	I <sub>D</sub>
-40V	15mΩ	-12A

#### **Applications**

- High Frequency Point-of-Load Synchronous Buck Converter for MB/NB/UMPC/VGA
- Networking DC-DC Power System
- Load Switch

#### **SOP-8L Pin Configuration**



#### **Absolute Maximum Ratings**

Symbol	Parameter Rating			
$V_{DS}$	Drain-Source Voltage -40			
$V_{GS}$	Gate-Source Voltage	±20	V	
I <sub>D</sub> @T <sub>A</sub> =25℃	Continuous Drain Current, V <sub>GS</sub> @ -10V <sup>1</sup>	-12	Α	
I <sub>D</sub> @T <sub>A</sub> =70°C	Continuous Drain Current, V <sub>GS</sub> @ -10V <sup>1</sup> -8.5			
I <sub>DM</sub> a	300µs Pulsed Drain Current (VGS=-10V) -40			
Eas <sup>b</sup>	Avalanche Energy, Single pulse (L=0.1mH) 50			
las <sup>b</sup>	Avalanche Current, Single pulse (L=0.1mH) -30			
P <sub>D</sub> @T <sub>A</sub> =25℃	Total Power Dissipation <sup>4</sup> 2.0			
T <sub>STG</sub>	Storage Temperature Range -55 to 150			
$T_J$	Operating Junction Temperature Range -55 to 150			

#### **Thermal Data**

Symbol	Parameter	Тур.	Max.	Unit
R <sub>0JA</sub>	Thermal Resistance Junction-Ambient <sup>1</sup>		70	°C/W
R <sub>0</sub> JC	Thermal Resistance Junction-Case <sup>1</sup>		25	°C/W





### Electrical Characteristics (T<sub>J</sub>=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit	
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =-250uA	-40			V	
$\triangle BV_{DSS}/\triangle T_{J}$	BV <sub>DSS</sub> Temperature Coefficient	Reference to 25℃ , I <sub>D</sub> =-1mA		-0.018		V/°C	
В	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =-10V , I <sub>D</sub> =-10A		15	19	mΩ	
$R_{DS(ON)}$		V <sub>GS</sub> =-4.5V , I <sub>D</sub> =-5A		19	26		
$V_{GS(th)}$	Gate Threshold Voltage	V <sub>GS</sub> =V <sub>DS</sub> . In =-250uA	-1.2	-1.7	-2.5	٧	
$\triangle V_{GS(th)}$	V <sub>GS(th)</sub> Temperature Coefficient	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =-2500A		5.04		mV/℃	
	Drain Source Leakage Current	V <sub>DS</sub> =-32V , V <sub>GS</sub> =0V , T <sub>J</sub> =25℃			-1		
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =-32V , V <sub>GS</sub> =0V , T <sub>J</sub> =55℃			-5	- uA	
I <sub>GSS</sub>	Gate-Source Leakage Current	$V_{GS}$ = $\pm 20V$ , $V_{DS}$ = $0V$			±100	nA	
gfs	Forward Transconductance	V <sub>DS</sub> =-5V , I <sub>D</sub> =-10A		18		S	
$Q_g$	Total Gate Charge (-4.5V)			28			
$Q_{gs}$	Gate-Source Charge	V <sub>DS</sub> =-20V , V <sub>GS</sub> =-10V , I <sub>D</sub> =-10A		2.8		nC	
$Q_{gd}$	Gate-Drain Charge			1.5			
T <sub>d(on)</sub>	Turn-On Delay Time			13.5			
Tr	Rise Time	V <sub>DD</sub> =-15V , V <sub>GS</sub> =-10V ,		20		no	
$T_{d(off)}$	Turn-Off Delay Time	$R_G=6\Omega$ , $I_D=-1A$		14		ns	
T <sub>f</sub>	Fall Time			26			
C <sub>iss</sub>	Input Capacitance			2700			
Coss	Output Capacitance	V <sub>DS</sub> =-15V , V <sub>GS</sub> =0V , f=1MHz		280		pF	
C <sub>rss</sub>	Reverse Transfer Capacitance			150			

#### **Diode Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
I <sub>S</sub>	Continuous Source Current <sup>1,6</sup>	\\ -\\ -0\\			-12	Α
I <sub>SM</sub>	Pulsed Source Current <sup>2,6</sup>	V <sub>G</sub> =V <sub>D</sub> =0V , Force Current			-40	Α
V <sub>SD</sub>	Diode Forward Voltage <sup>2</sup>	V <sub>GS</sub> =0V , I <sub>S</sub> =-1A , T <sub>J</sub> =25℃			-1.1	V
t <sub>rr</sub>	Reverse Recovery Time	  -   IF=-10A,dI/dt=100A/µs,TJ=25℃		26		nS
Q <sub>rr</sub>	Reverse Recovery Charge			20		nC

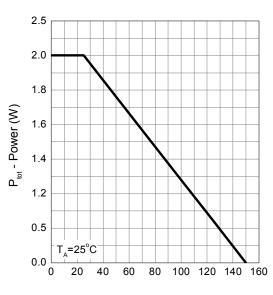
#### Note

- 1,Pulse test; pulse width≤300µs, duty cycle≤2%.
- 2, Guaranteed by design, not subject to production testing.



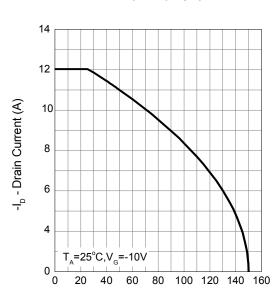
## **Typical Operating Characteristics**

# Power Dissipation



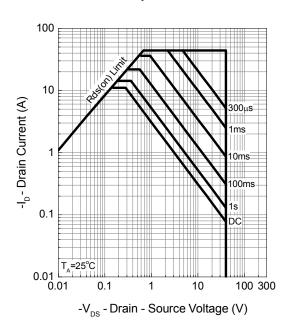
T<sub>i</sub> - Junction Temperature (°C)

#### **Drain Current**

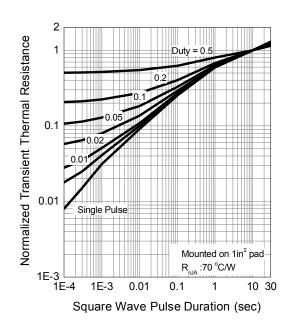


T<sub>i</sub> - Junction Temperature (°C)

### **Safe Operation Area**

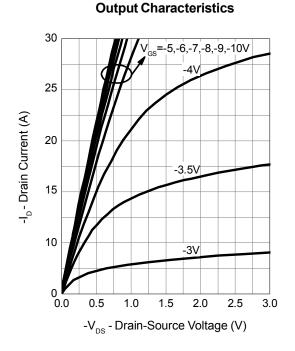


#### **Thermal Transient Impedance**

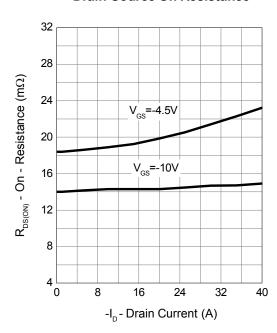




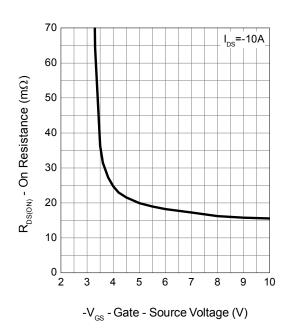
### **Typical Operating Characteristics**



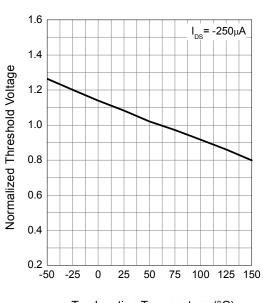
#### **Drain-Source On Resistance**



#### **Gate-Source On Resistance**

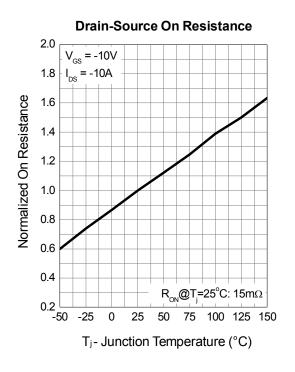


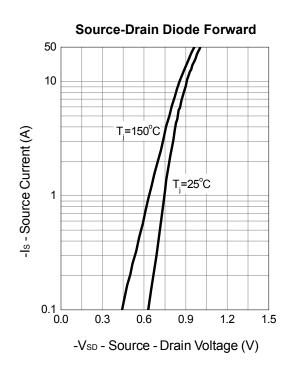
#### **Gate Threshold Voltage**

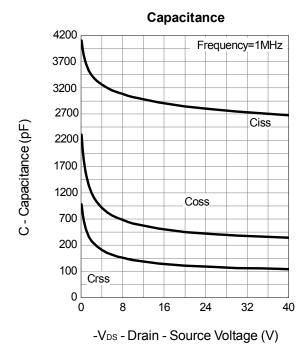


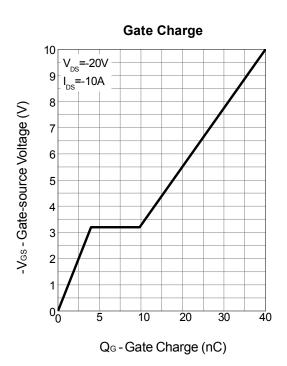


## **Typical Operating Characteristics**



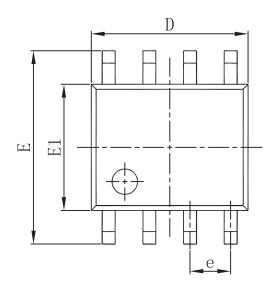


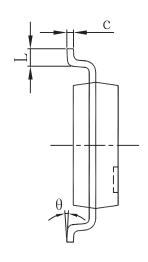


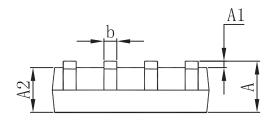




### **Packaging information**







Cross hoal	Dimensions In Millimeters		Dimensions In Inches		
Symbol	Min	Max	Min	Max	
A	1.350	1.750	0.053	0.069	
A1	0.100	0. 250	0.004	0.010	
A2	1. 350	1. 550	0. 053	0.061	
b	0.330	0. 510	0. 013	0.020	
С	0. 170	0. 250	0.007	0.010	
D	4.800	5. 000	0. 189	0. 197	
e	1. 270 (BSC)		0.050 (BSC)		
Е	5. 800	6. 200	0. 228	0. 244	
E1	3.800	4. 000	0. 150	0. 157	
L	0.400	1. 270	0.016	0.050	
θ	0°	8°	0°	8°	



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