**N-Ch MOSFET** 

### **General Description**

The WST2088 is the highest performance trench N-ch MOSFETs with extreme high cell density , which provide excellent  $R_{\text{DSON}}$  and gate charge for most of the small power switching and load switch applications.

The WST2088 meet the RoHS and Green Product requirement with full function reliability approved.

#### **Features**

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent Cdv/dt effect decline
- Green Device Available

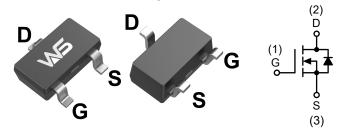
### **Product Summery**

BV <sub>DSS</sub>	R <sub>DSON</sub>	I <sub>D</sub>
20V	8mΩ	8.8A

## **Applications**

- Power switching application
- Hard Switched and High Frequency Circuits
- Uninterruptible Power Supply

### **SOT-23-3L Pin Configuration**



### **Absolute Maximum Ratings**

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	20	V
$V_{GS}$	Gate-Source Voltage	±12	V
I <sub>D</sub> @T <sub>c</sub> =25℃	Continuous Drain Current, V <sub>GS</sub> @ 4.5V	8.8	Α
I <sub>D</sub> @T <sub>c</sub> =70℃	Continuous Drain Current, V <sub>GS</sub> @ 4.5V	6.2	А
I <sub>DP</sub>	Pulsed Drain Current	40	Α
P <sub>D</sub> @T <sub>A</sub> =25°C	Total Power Dissipation	1.5	W
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	$^{\circ}$
TJ	Operating Junction Temperature Range	-55 to 150	$^{\circ}$

### **Thermal Data**

Symbol	Parameter		Max.	Unit
Rthj-a	Maximum Thermal Resistance, Junction-ambient		25	°C/W
Rthj-c	Maximum Thermal Resistance, Junction-case		8	°C/W



## Electrical Characteristics (T<sub>J</sub>=25 ℃, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	Drain-Source Breakdown Voltage V <sub>GS</sub> =0V , I <sub>D</sub> =250uA				V
$\triangle BV_{DSS}/\triangle T_{J}$	BVDSS Temperature Coefficient	Reference to 25℃ , I <sub>D</sub> =1mA		0.018		V/℃
Б	Static Dunin Source On Desistance <sup>2</sup>	V <sub>GS</sub> =4.5V , I <sub>D</sub> =6A		8	13	0
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =2.5V , I <sub>D</sub> =5A	10 19 mΩ		mc2	
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250uA	0.5	0.7	1.3	V
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =16V , V <sub>GS</sub> =0V.			10	uA
I <sub>GSS</sub>	Gass Gate-Source Leakage Current V <sub>GS</sub> =±12V , V <sub>DS</sub> =0V				±100	nA
Qg	Total Gate Charge			16		
$Q_{gs}$	Gate-Source Charge	V <sub>DS</sub> =15V , V <sub>GS</sub> =4.5V , I <sub>D</sub> =6A		3		nC
$Q_{gd}$	Gate-Drain Charge			4.5		
$T_{d(on)}$	Turn-On Delay Time			10		
Tr	Rise Time	V <sub>DS</sub> =10V , V <sub>GS</sub> =4.5V ,		13		no
$T_{d(off)}$	Turn-Off Delay Time	$R_G=3.3\Omega$ $I_D=1A$		7		ns
T <sub>f</sub>	Fall Time			28		
C <sub>iss</sub>	Input Capacitance			1400		
Coss	Output Capacitance	V <sub>DS</sub> =15V , V <sub>GS</sub> =0V , f=1MHz		170		pF
C <sub>rss</sub>	Reverse Transfer Capacitance			135		

### **Diode Characteristics**

Symbol	Parameter Conditions		Min.	Тур.	Max.	Unit
V <sub>SD</sub>	Diode Forward Voltage	V <sub>GS</sub> =0V , I <sub>S</sub> =1A			1.2	V
t <sub>rr</sub>	Reverse Recovery Time	IF=1A , V <sub>GS</sub> =0V, dI/dt=100A/µs		8.5		nS
Qrr	Reverse Recovery Charge	17 τ, v <sub>GS</sub> σν, απατ-100/νμσ		2.5		nC

#### Notes:

1. Pulse width limited by Max. junction temperature.

2.Pulse test

3.Surface mounted on 1 in 2 copper pad of FR4 board, t  $\leq\!\!10 sec$  ; 60 °C/W at steady state.

4.Starting  $T_j{=}25^{\circ}C$  ,  $V_{DD}{=}20V$  , L=0.1mH ,  $R_G{=}25\Omega,\,V_{GS}{=}10V$ 



## **Typical Characteristics**

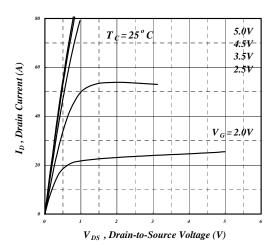


Fig 1. Typical Output Characteristics

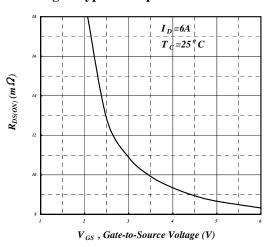


Fig 3. On-Resistance v.s. Gate Voltage

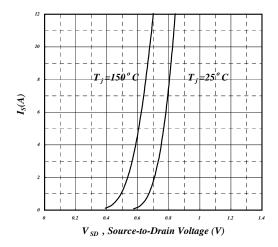


Fig 5. Forward Characteristic of Reverse Diode

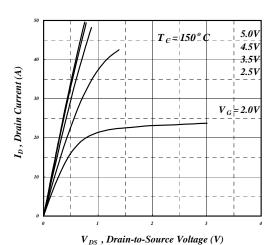


Fig 2. Typical Output Characteristics

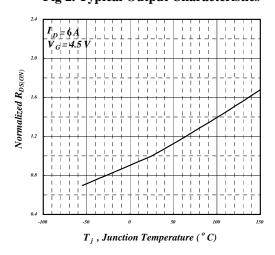


Fig 4. Normalized On-Resistance v.s. Junction Temperature

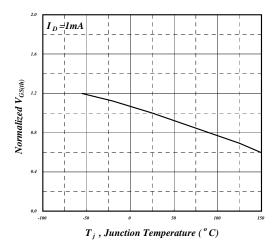


Fig 6. Gate Threshold Voltage v.s.
Junction Temperature



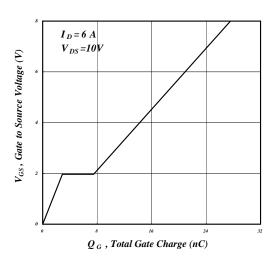


Fig 7. Gate Charge Characteristics

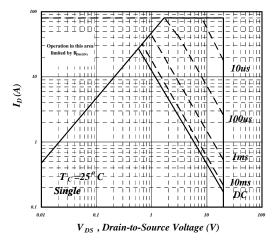


Fig 9. Maximum Safe Operating Area

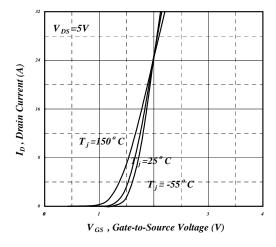


Fig 11. Transfer Characteristics

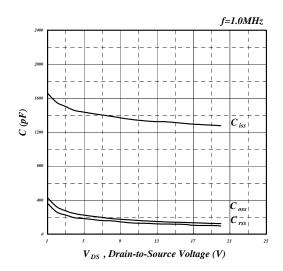


Fig 8. Typical Capacitance Characteristics

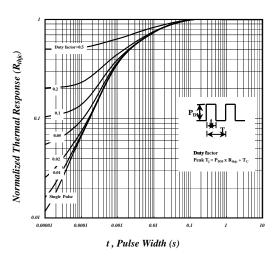


Fig 10. Effective Transient Thermal Impedance

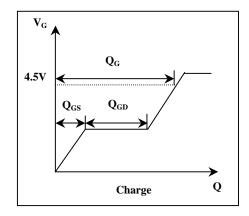
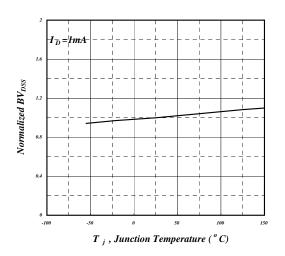


Fig 12. Gate Charge Waveform





 $\label{eq:posterior} \textbf{Fig 13. Normalized BV}_{DSS} \ \ \textbf{v.s. Junction} \\ \textbf{Temperature}$ 

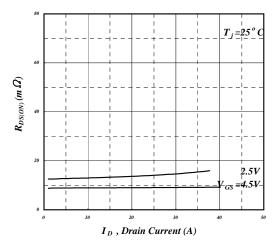


Fig 15. Typ. Drain-Source on State Resistance

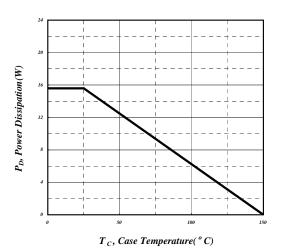
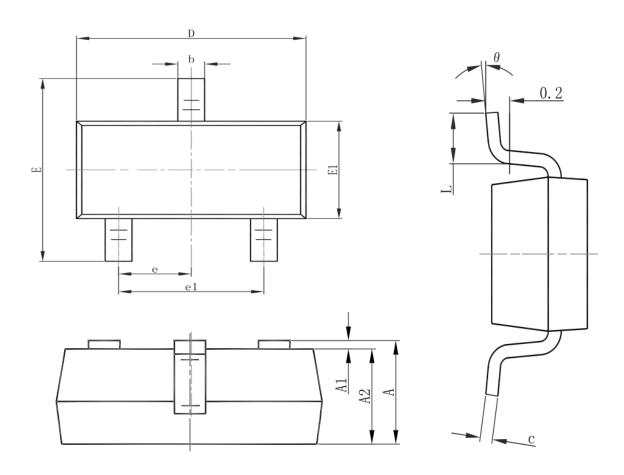


Fig 14. Total Power Dissipation



# **Packaging information**



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
Α	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
С	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E1	1.500	1.700	0.059	0.067
E	2.650	2.950	0.104	0.116
е	0.950	(BSC)	0.037(BSC)	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°



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