

N-Channel MOSFET

General Description

The WSD3072DN33 is the highest performance trench N-Channel MOSFET with extreme high cell density, which provide excellent $R_{DS(ON)}$ and gate charge for most of the synchronous buck converter applications.

The WSD3072DN33 meet the RoHS and Green Product requirement, 100% E_{AS} guaranteed with full function reliability approved.

Features

- 100% UIS Tested.
- Reliable and Rugged
- Lead Free and Green Devices Available (RoHS Compliant)

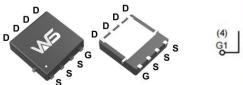
Product Summery

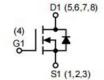
BV _{DSS}	R _{DS(ON)}	I _D
30V	5.6mΩ	70A

Applications

- Power Management for Industrial DC/DC Converters
- Ldeal for high-frequency switching and synchronous rectification

DFN3*3-8L Pin Configuration





Absolute Maximum Ratings (T_A=25°C, Unless Otherwise Noted)

Symbol	Parameter		Rating	Units
V _{DS}	Drain-Source Voltage		30	V
V _{GS}	Gate-Source Voltage		±20	V
1 7	Cantinua Dania Cumant	T _C =25°C	70	
I _D ⁷	Continuous Drain Current	T _C =100°C	52	A
I _{DM} ³	Pulse Drain Current		100	
P _D ²	Power Dissipation	T _C =25°C	28	W
I _{AS} ³	Single pulse Avalanche Current		63	А
E _{AS} ³	Single pulse Avalanche Energy	L=0.5mH	110	mJ
T _{STG}	Storage Temperature Range		-55 to 150	°C
TJ	Operating Junction Temperature Range		-55 to 150	
D 14	T. 15	t≤10s	30	
R _{θJA} ^{1,4}	Thermal Resistance-Junction to Ambient	Steady State	60.5	°C/W
R _{θJC}	Thermal Resistance-Junction to Case		4.5	



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Electrical Characteristics (T_J=25°C, Unless Otherwise Noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =250μA	30			V
		V _{GS} =10V , I _D =20A		5.6	6.7	
R _{DS(ON)}	Static Drain-Source On-Resistance	T _J =125°C		9.5		mΩ
		V_{GS} =4.5V , I_D =15A		7.6	9.3	
$V_{GS(th)}$	Gate Threshold Voltage	V_{GS} = V_{DS} , I_D =250 μ A	1.0	1.5	2.2	V
	Drain-Source Leakage Current	V _{DS} =30V , V _{GS} =0V			1.0	
I _{DSS}	Diani-Source Leakage Current	T _J =55°C			5.0	μA
I _{GSS}	Gate-Source Leakage Current	V_{DS} =0V , V_{GS} =±10V			±100	nA
9 _{fs}	Forward Transconductance	V_{DS} =5V , I_{D} =20A		8		S
R_G	Gate Resistance	f=1.0MHz	1.0	2.0	3.0	Ω
Q_g	Total Gate Charge (10V)			45		
Q_{gs}	Gate-Source Charge	V_{DS} =15V , V_{GS} =10V , I_{D} =20A		9.5		nC
Q_{gd}	Gate-Drain Charge			7.8		
T _{d(on)}	Turn-On Delay Time			18		
T _r	Rise Time	V_{DD} =5V , V_{GS} =10V , I_{D} =20A		10		no
$T_{d(off)}$	Turn-Off Delay Time	$R_{I}=1\Omega$, $R_{GEN}=3\Omega$		34		ns
T _f	Fall Time]		10		
C _{iss}	Input Capacitance			1780		
C _{oss}	Output Capacitance	V_{DS} =15V , V_{GS} =0V , f =1.0MHz		266		pF
C _{rss}	Reverse Transfer Capacitance			210		

Diode Characteristics

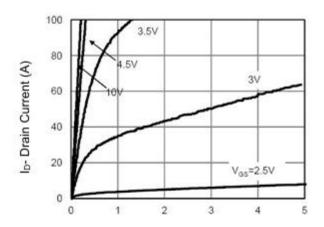
Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units
I _S ⁷	Continuous Source Current				70	Α
V _{SD}	Diode Forward Voltage	V _{GS} =0V , I _S =1A		0.7	1.2	V
t _{rr}	Reverse Recovery Time	I _E =20A , di/dt=500A/µs		47		ns
Q _{rr}	Reverse Recovery Charge	1 _F -20A , αι/αι-300A/μs		25		nC

Note:

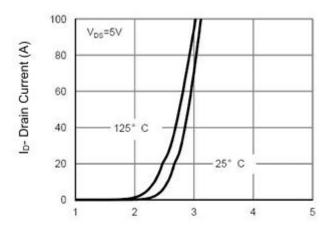
- 1. The value of R_{BJA} is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with T_A=25°C. The Power dissipation P_{DSM} is based on R_{BJA} t≤ 10s and the maximum allowed junction temperature of 150°C. The value in any given application depends on the user's specific board design.
- 2. The power dissipation P_D is based on T_{J(MAX)}=150°C, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.
- 3. Single pulse width limited by junction temperature $T_{J(MAX)}$ =150°C.
- 4. The $R_{\theta JA}$ is the sum of the thermal impedance from junction to case $R_{\theta JC}$ and case to ambient.
- 5. The static characteristics in Figures 1 to 6 are obtained using $<300\mu s$ pulses, duty cycle 0.5% max.
- 6. These curves are based on the junction-to-case thermal impedance which is measured with the device mounted to a large heatsink, assuming a maximum junction temperature of T_{J(MAX)}=150°C. The SOA curve provides a single pulse rating.
- 7. The maximum current rating is package limited.
- 8. These tests are performed with the device mounted on 1 in² FR-4 board with 2oz. Copper, in a still air environment with T_A=25°C.
- 9. The maximum current rating is silicon limited



Typical Characteristics



Vds Drain-Source Voltage (V)
Figure 1 Output Characteristics



Vgs Gate-Source Voltage (V)

Figure 2 Transfer Characteristics

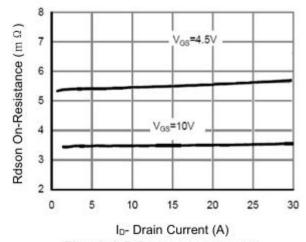


Figure 3 Rdson- Drain Current

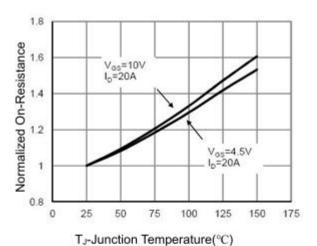
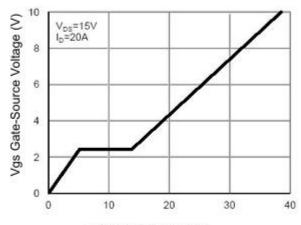


Figure 4 Rdson-Junction Temperature



Qg Gate Charge (nC)

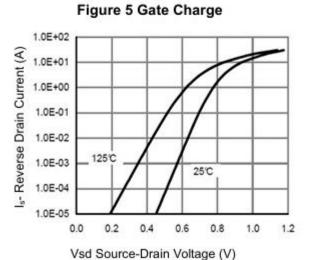


Figure 6 Source- Drain Diode Forward



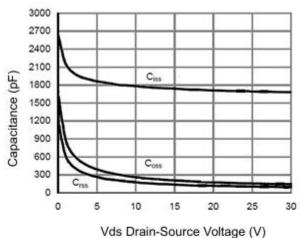
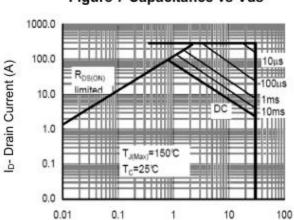
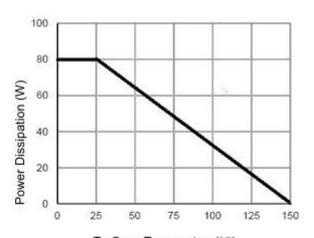


Figure 7 Capacitance vs Vds



Vds Drain-Source Voltage (V)
Figure 8 Safe Operation Area



T_C-Case Temperature(°C)
Figure 9 Power De-rating

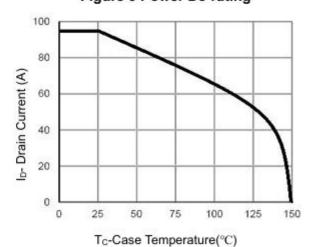


Figure 10 Current De-rating

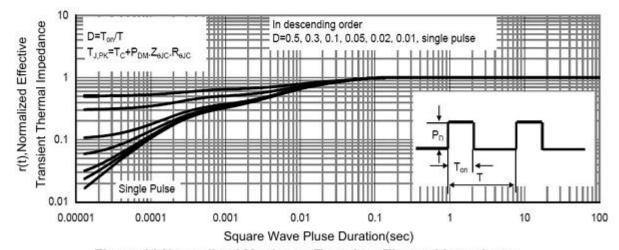
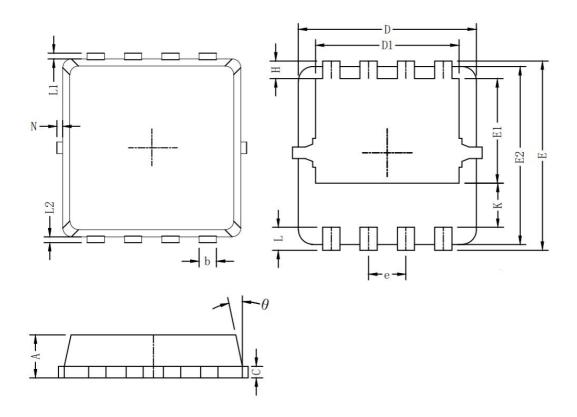


Figure 11 Normalized Maximum Transient Thermal Impedance



Packaging information



Symbol	Dim in mm		
	min	typ	max
Α	0.6	0.75	0.9
b	0.2	0.3	0.4
С	0.15	0.2	0.25
D	3	3.1	3.2
D1	2.3	2.45	2.6
E	3.15	3.3	3.45
E1	1.43	1.73	1.93
E2	2.9	3.05	3.2
е	0.65BSC		
Н	0.2	0.35	0.5
K	0.57	0.77	0.87
L	0.3	0.4	0.5
L1/L2	0.1REF		
θ	8°	10°	13°
N	0		0.15



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