

General Description

The WSP4408 is the highest performance trench N-Channel MOSFET with extreme high cell density, which provide excellent $R_{DS(ON)}$ and gate charge for most of the synchronous buck converter applications.

The WSP4408 meet the RoHS and Green Product requirement, 100% E_{AS} guaranteed with full function reliability approved.

Features

- 100% UIS Tested.
- Reliable and Rugged
- Lead Free and Green Devices Available (RoHS Compliant)

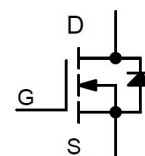
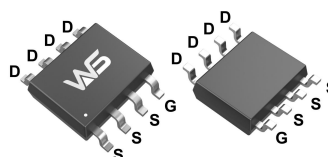
Product Summery

BV_{DSS}	$R_{DS(ON)}$	I_D
30V	9.5m Ω	11A

Applications

- Power Management for Industrial DC/DC Converters

SOP-8L Pin Configuration



Absolute Maximum Ratings ($T_A=25^{\circ}\text{C}$, Unless Otherwise Noted)

Symbol	Parameter		Rating	Units
V_{DS}	Drain-Source Voltage		30	V
V_{GS}	Gate-Source Voltage		± 20	
I_D^7	Continuous Drain Current	$T_C=25^{\circ}\text{C}$	11	A
		$T_C=100^{\circ}\text{C}$	7	
I_{DM}^3	Pulse Drain Current		40	
P_D^2	Power Dissipation	$T_C=25^{\circ}\text{C}$	3.1	W
		$T_C=100^{\circ}\text{C}$	1.0	
I_{AS}^3	Single pulse Avalanche Current		20	A
E_{AS}^3	Single pulse Avalanche Energy	$L=0.3\text{mH}$	23	mJ
T_{STG}	Storage Temperature Range		-55 to 150	$^{\circ}\text{C}$
T_J	Operating Junction Temperature Range		-55 to 150	
$R_{\theta JA}^{1,4}$	Thermal Resistance-Junction to Ambient	$t \leq 10\text{s}$	30	$^{\circ}\text{C/W}$
		Steady State	65	
$R_{\theta JC}$	Thermal Resistance-Junction to Case		20	

Electrical Characteristics ($T_J=25^{\circ}\text{C}$, Unless Otherwise Noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Units
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V$, $I_D=250\mu A$	30	---	---	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance	$V_{GS}=10V$, $I_D=11A$	---	9.5	11.8	m Ω
		$T_J=125^{\circ}\text{C}$	---	15	17	
		$V_{GS}=4.5V$, $I_D=10A$	---	12	15	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$, $I_D=250\mu A$	1.0	1.5	2.5	V
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=24V$, $V_{GS}=0V$	---	---	1.0	μA
		$T_J=55^{\circ}\text{C}$	---	---	5.0	
I_{GSS}	Gate-Source Leakage Current	$V_{DS}=0V$, $V_{GS}=\pm 20V$	---	---	± 100	nA
g_{fs}	Forward Transconductance	$V_{DS}=5V$, $I_D=20A$	---	50	---	S
R_G	Gate Resistance	$f=1.0\text{MHz}$	1.0	2.0	3.1	Ω
Q_g	Total Gate Charge (4.5V)	$V_{DS}=15V$, $V_{GS}=4.5V$, $I_D=11A$	---	8	---	nC
Q_{gs}	Gate-Source Charge		---	16	---	
Q_{gd}	Gate-Drain Charge		---	25	---	
$T_{d(on)}$	Turn-On Delay Time	$V_{DS}=15V$, $V_{GS}=10V$, $R_L=1\Omega$, $R_{GEN}=3\Omega$	---	12	---	ns
T_r	Rise Time		---	9.5	---	
$T_{d(off)}$	Turn-Off Delay Time		---	25	---	
T_f	Fall Time		---	5.5	---	
C_{iss}	Input Capacitance	$V_{DS}=20V$, $V_{GS}=0V$, $f=1.0\text{MHz}$	---	950	---	pF
C_{oss}	Output Capacitance		---	150	---	
C_{rss}	Reverse Transfer Capacitance		---	90	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Units
I_S ⁷	Continuous Source Current		---	---	11	A
V_{SD}	Diode Forward Voltage	$V_{GS}=0V$, $I_S=1A$	---	0.7	1.0	V
t_{rr}	Reverse Recovery Time	$I_F=11A$, $di/dt=500A/\mu s$	---	20	---	ns
Q_{rr}	Reverse Recovery Charge		---	15	---	nC

Note:

- The value of $R_{\theta JA}$ is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with $T_A=25^{\circ}\text{C}$. The Power dissipation P_{DSM} is based on $R_{\theta JA} \leq 10s$ and the maximum allowed junction temperature of 150°C . The value in any given application depends on the user's specific board design.
- The power dissipation P_D is based on $T_{J(MAX)}=150^{\circ}\text{C}$, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.
- Single pulse width limited by junction temperature $T_{J(MAX)}=150^{\circ}\text{C}$.
- The $R_{\theta JA}$ is the sum of the thermal impedance from junction to case $R_{\theta JC}$ and case to ambient.
- The static characteristics in Figures 1 to 6 are obtained using $<300\mu s$ pulses, duty cycle 0.5% max.
- These curves are based on the junction-to-case thermal impedance which is measured with the device mounted to a large heatsink, assuming a maximum junction temperature of $T_{J(MAX)}=150^{\circ}\text{C}$. The SOA curve provides a single pulse rating.
- The maximum current rating is package limited.
- These tests are performed with the device mounted on 1 in² FR-4 board with 2oz. Copper, in a still air environment with $T_A=25^{\circ}\text{C}$.
- The maximum current rating is silicon limited

Typical Characteristics

Typical Characteristics

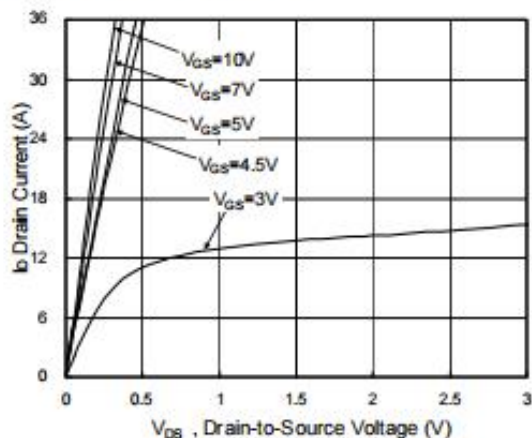


Fig.1 Typical Output Characteristics

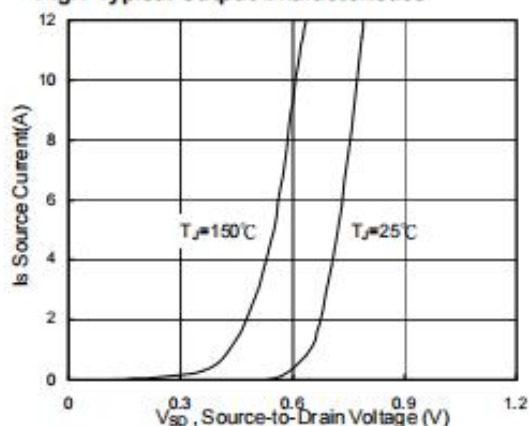


Fig.3 Forward Characteristics of Reverse

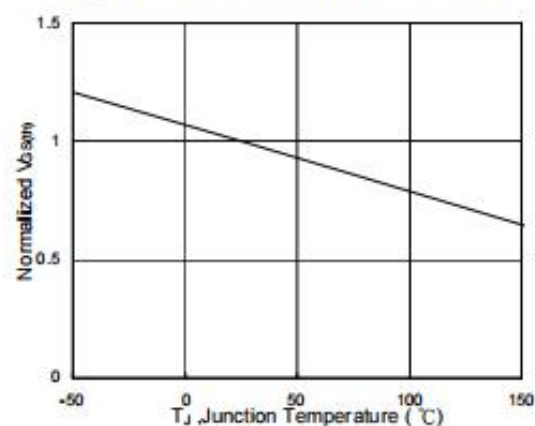


Fig.5 Normalized $V_{GS(th)}$ vs. T_J

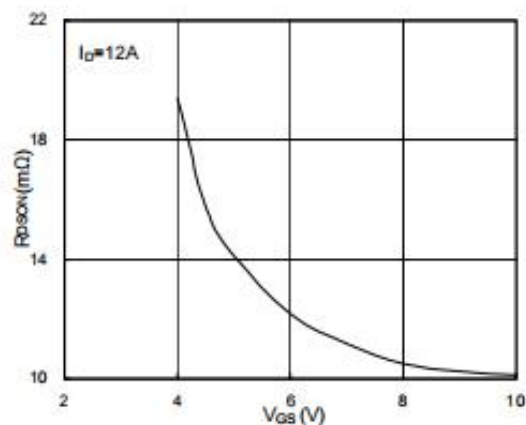


Fig.2 On-Resistance vs. G-S Voltage

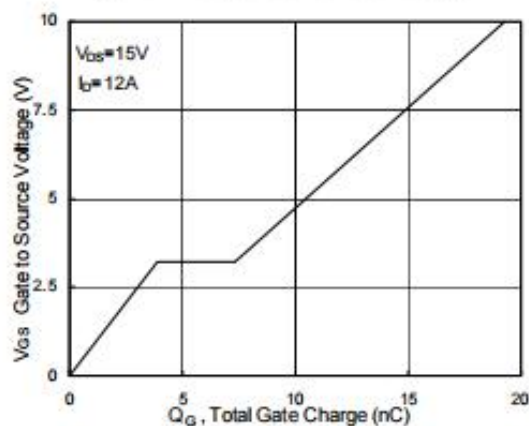


Fig.4 Gate-Charge Characteristics

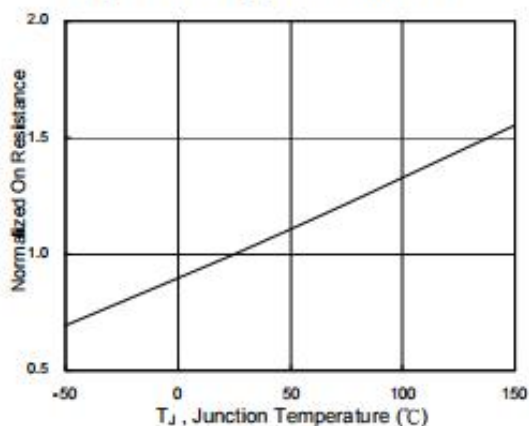


Fig.6 Normalized $R_{DS(on)}$ vs. T_J

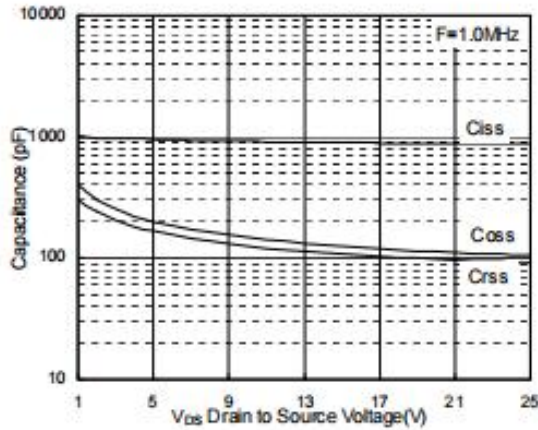


Fig.7 Capacitance

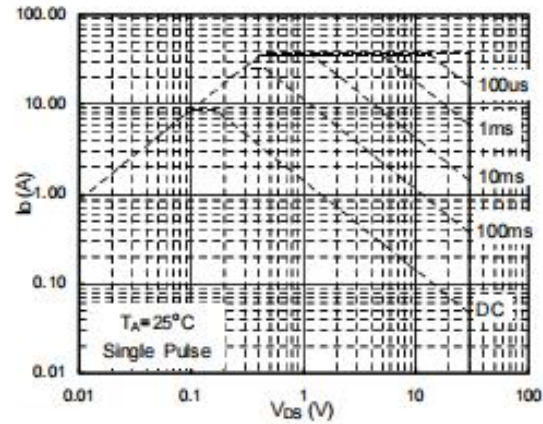


Fig.8 Safe Operating Area

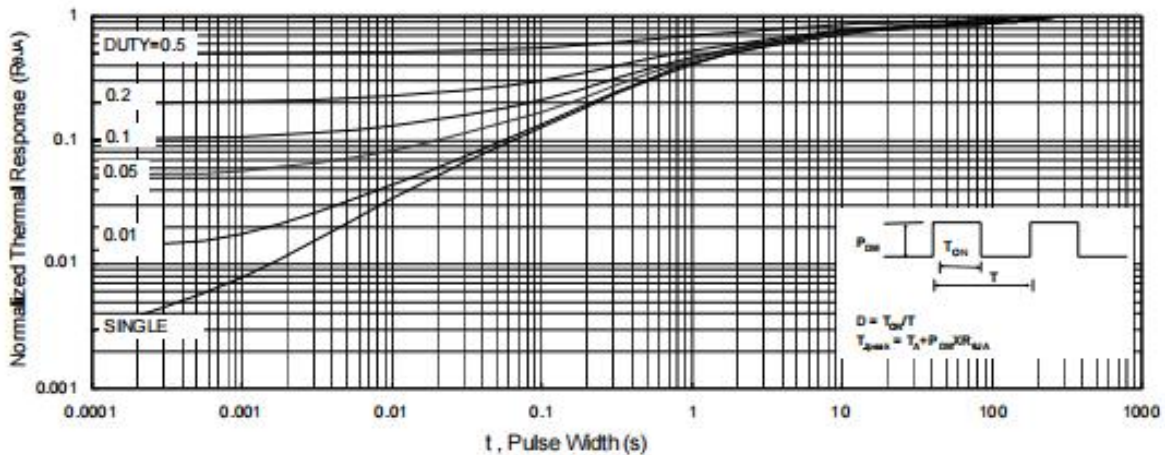


Fig.9 Normalized Maximum Transient Thermal Impedance

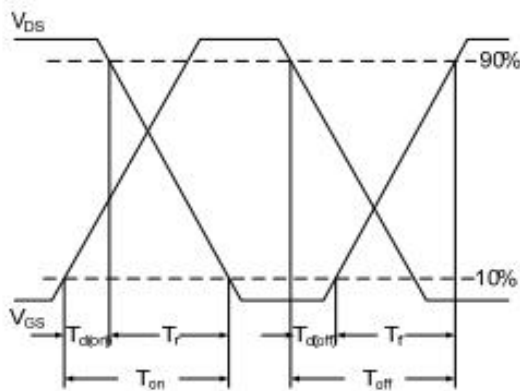


Fig.10 Switching Time Waveform

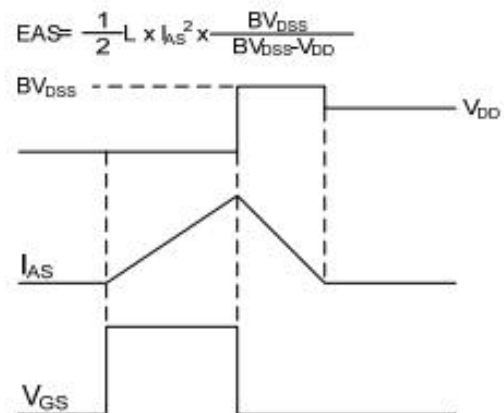
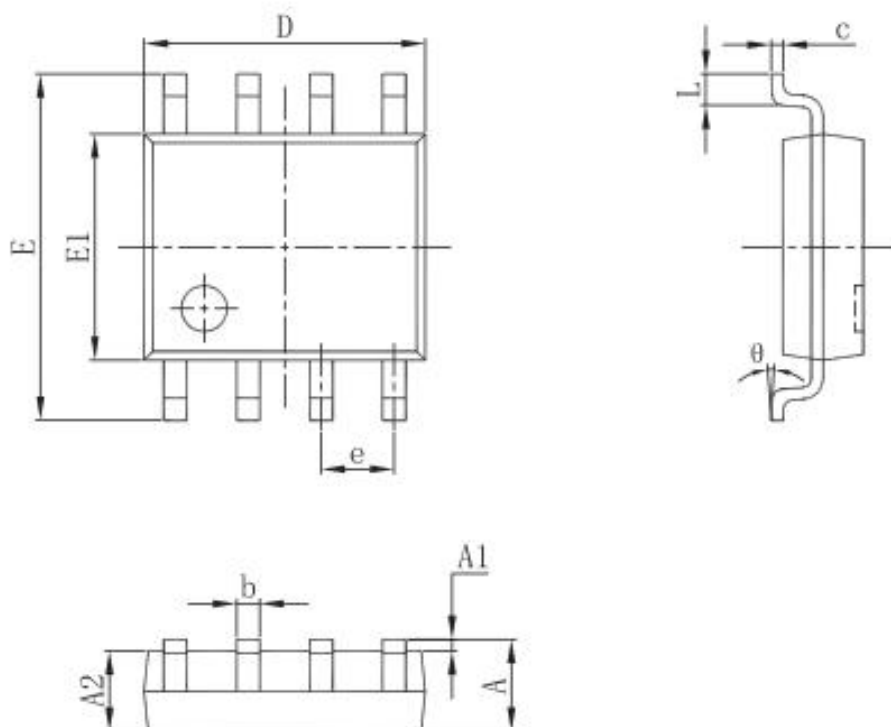


Fig.11 Unclamped Inductive Switching Waveform

Packaging information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.007	0.010
D	4.800	5.000	0.189	0.197
e	1.270 (BSC)		0.050 (BSC)	
E	5.800	6.200	0.228	0.244
E1	3.800	4.000	0.150	0.157
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°

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